

[illegible][illegible]

DWG ISS	CD ISSUE	DWG ISS	CD ISSUE	DWG ISS	CD ISSUE
1	1	2B	2B	3D	2B APP 10
4D	2B APP 20	5D	APP 3D	6B	2B APP 4B
DWG ISSUE	CD ISSUE	DATE ISSUED		Designs	APPRO
7B	3B	1-6-72		LFS GBH	NRB FLS AFR

1. WHEN CHANGES ARE MADE IN THIS DRAWING, ONLY THOSE SHEETS AFFECTED WILL BE REISSUED.	CATEGORY	NO.
2. THIS SHEET INDEX WILL BE REISSUED AND BROUGHT UP TO DATE EACH TIME ANY SHEET OF THE DRAWING IS REISSUED, OR A NEW SHEET IS ADDED.	EQUIPMENT, DRAWINGS MAINTENANCE BSP	J58837AD
3. THE ISSUE NUMBER ASSIGNED TO A CHANGED OR NEW SHEET WILL BE THE SAME ISSUE NUMBER AS THAT OF THE SHEET INDEX.		540-560-501
4. SHEETS THAT ARE NOT CHANGED WILL RETAIN THEIR EXISTING ISSUE NUMBER.		
5. THE LAST ISSUE NUMBER OF THE SHEET INDEX IS RECOGNIZED AS THE LATEST ISSUE NUMBER OF THE DRAWING AS A WHOLE.		

(TST CKT FOR REG SDR & LNK)

BELL TELEPHONE LABORATORIES
INCORPORATED

SD-65827-01-AI
10 SHEETS

APPARATUS INDEX

DESIG	LOCATION		
	FS	APP FIG	EQPT
RELAYS			
C CLA2	1G1 2C3	1 1	
P	2A4	1	
P1	1A1	1	
P2	1B1	1	
P3	1B1	1	
P4	1D1	1	
P5	1F1	1	
SC	2E3	1	

CORDS			
-	2B7	2	
-	2D7	3	
PATCH CORD	3A4	5	
PATCH CORD 1	2B7, 2B9	4	
PATCH CORD 2	2D7, 2D9	4	

DIODES			
A	1G6	1	
B	1G6	1	
C CS	1G6 2C3	1 1	
D	1F6	1	
E	1F6	1	
F	1F6	1	
G	1E6	1	
H	1E6	1	
J	1E6	1	
K	1D6	1	

DESIG	LOCATION		
	FS	APP FIG	EQPT
JACK			
A	2B6, 2C6	1	
B	2B6, 2C6	1	
C	2D6, 2E6	1	
D	2D6, 2E6	1	
PPS	2A6	1	

KEY			
CLA CMS CT	SEE APP FIG.	1	
PUSH BUTTON DIAL	2B1	1	
RV	SEE APP FIG.	1	

LAMPS			
CS	2D4	1	
DIGIT 0	1C6	1	
1	1A6	1	
2	1B6	1	
3	1B6	1	
4	1B6	1	
5	1A6	1	
6	1C6	1	
7	1D6	1	
8	1D6	1	
DIGIT 9	1C6	1	

DESIG	LOCATION		
	FS	APP FIG	EQPT
LAMPS (CONT)			
DTM	2C4	1	
RL	2B0	1	
SDR 0	1G6	1	
1	1G6	1	
2	1G6	1	
3	1F6	1	
4	1F6	1	
5	1F6	1	
6	1E6	1	
7	1E6	1	
8	1E6	1	
SDR 9	1D6	1	

PLUG			
A & B	2B7, 2C7	2, 4	
C & D	2D7, 2E7	3, 4	
L	2B8	2, 4	
R	2D8	3, 4	

RESISTORS			
A	2C5	1	
B	2D3	1	
HG	2B5	1	
P1	1B3	1	
P2	1B3	1	
RL	2C3	1	

DESIG	LOCATION		
	FS	APP FIG	EQPT
SOCKET			
SRO SR1 SR2-9	3B6 3B6 3A6	5 5 5	
TR	3B3	5	

LEAD INDEX

DESIG	LOCATION	
	FS	CAD
REGISTER SENDER CIRCUIT		
0	3B7	1D6
1	3B7	1D6
2	3B7	1D6
4	3B7	1D6
7	3B7	1D6
CLA CLA2 CS	3B7 3B7 3B7	1D6 1D6 1E0, 1A3 1D6 1D6
CT	3B7	1D6
KR	3B7	1D6
R	3B7	1D6
SD-9	1F7	1A3, 1E0 1C6
T	3B7	1D6

OPTION INDEX

APP OR WRG	LOCATION	
	FS	CAD
2	2B7, 2C7	2
3	2D7, 2E7	3
4	2B7, 2C7, 2D7, 2E7	4
5	2F6, 3B2	5
1		
2	2A5, 2B5	1
Y	2A5, 2A6, 2B6	1
X	2D3	1, 2
W	2D4	1, 2
V	2C2	1
T	2C2	1
S	2D4	1
R	2D4	1
Q	2B6, 2B7, 2C6, 2D6, 2D7, 2E6	4
N	2B6, 2C6, 2D6, 2E6	4
M	2B3, 2B4, 2C2, 2C4	1
K	2B3, 2B4, 2C3, 2C4	1
E	2C2	1

TEST CIRCUIT

2

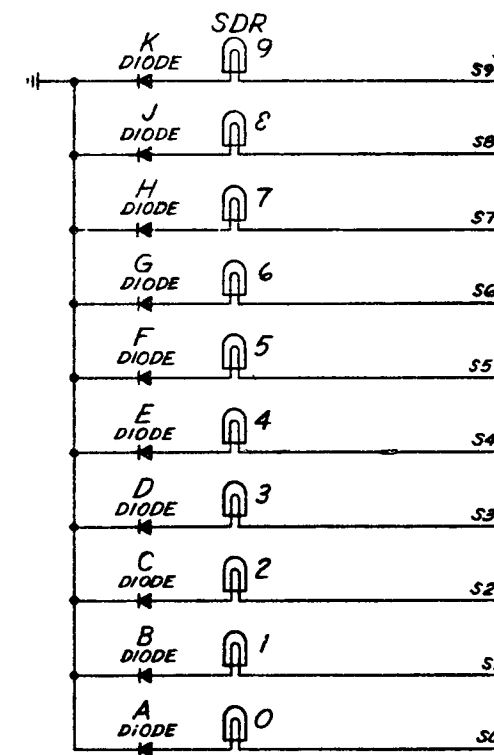
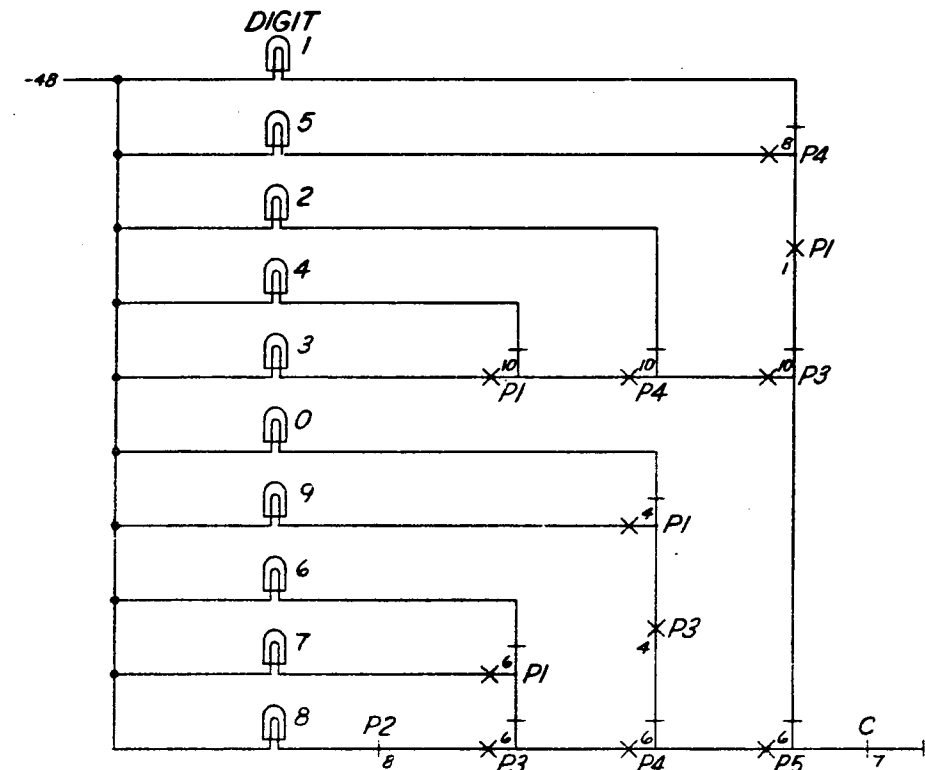
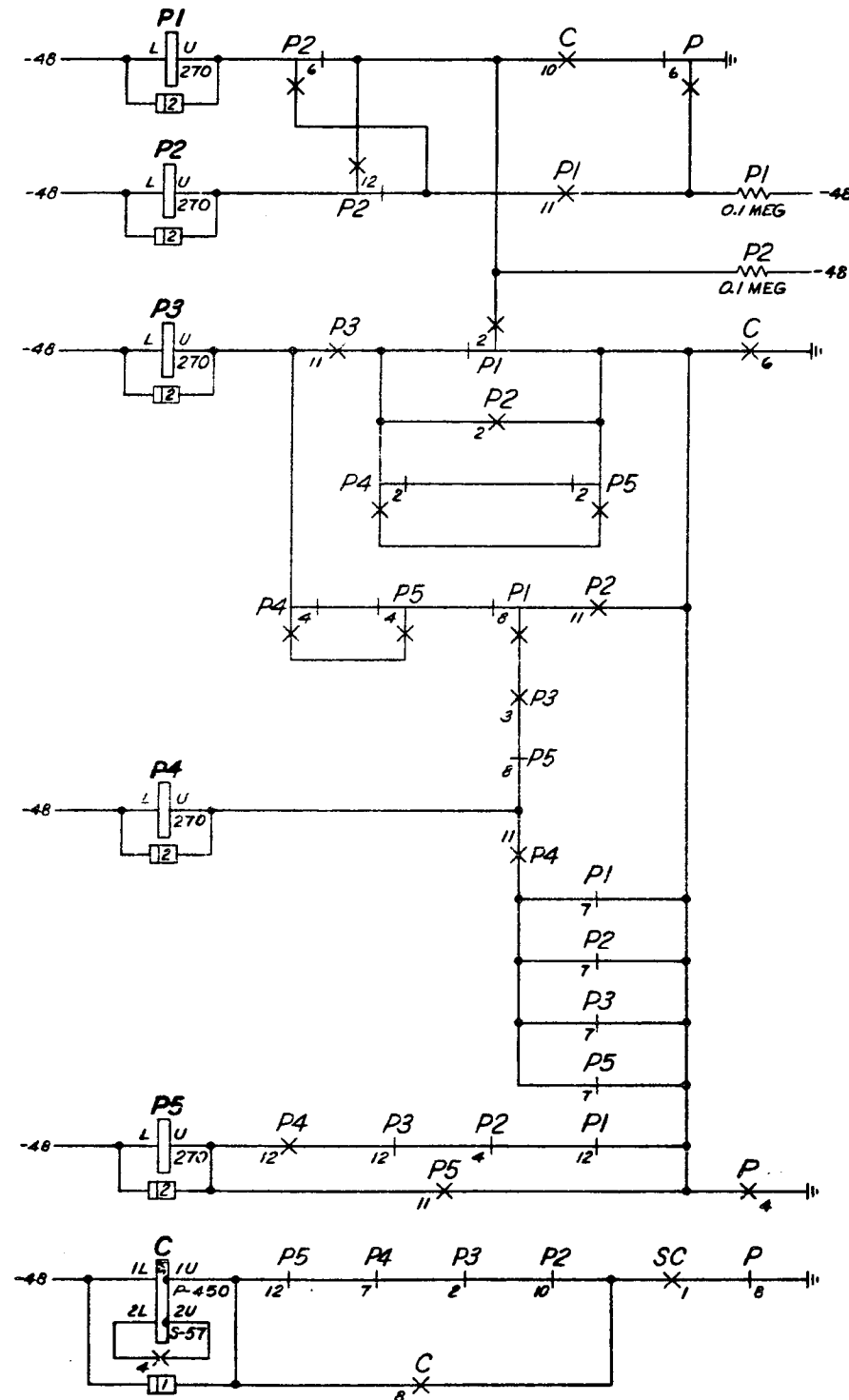
SD-65827-01-A2

BELL TELEPHONE LABORATORIES
INCORPORATED

6S

PRINTED IN U.S.A.

PART OF FS 1
TEST CIRCUIT

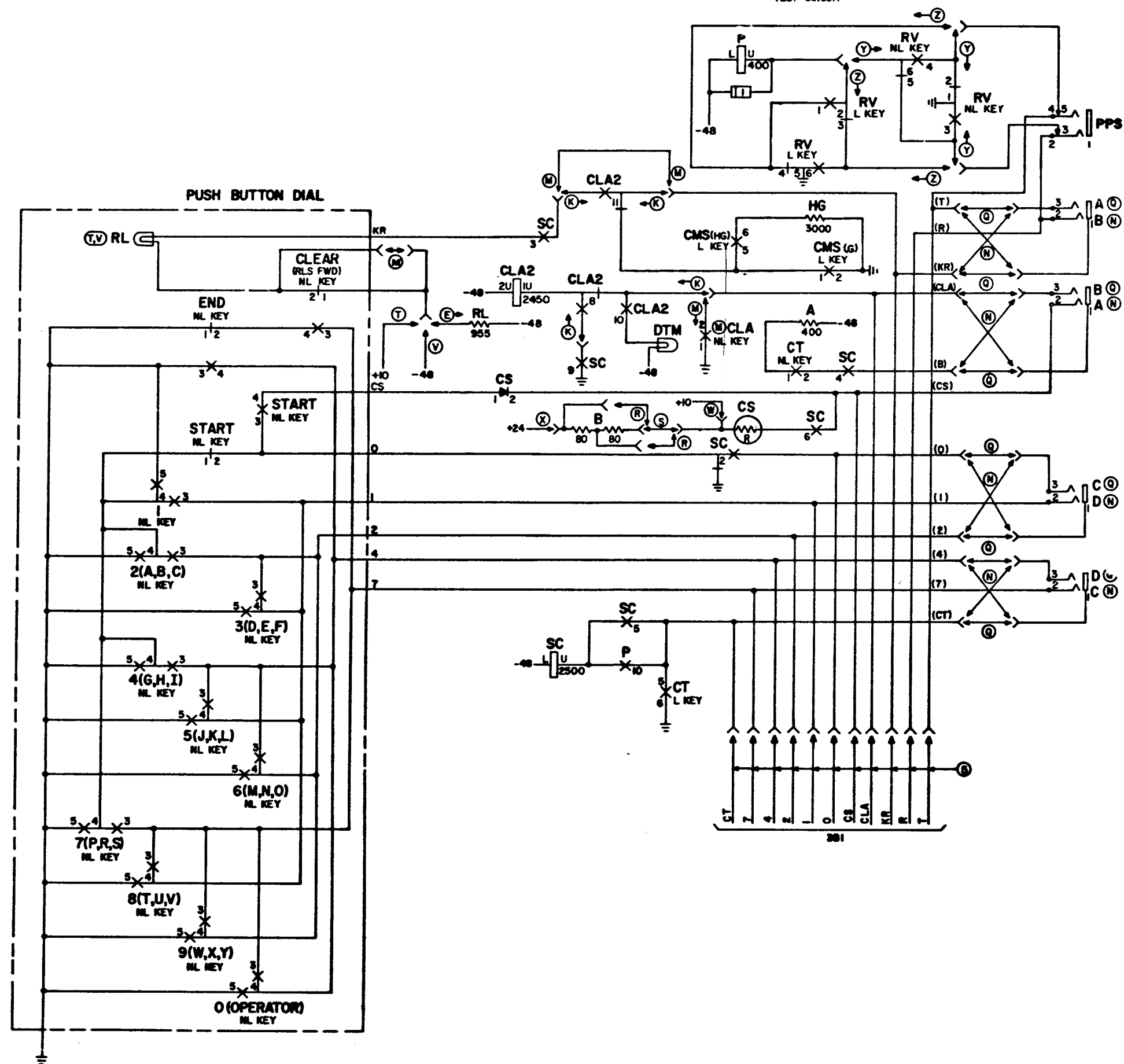


TO REGISTER SENDER
CIRCUIT OR 387
(SEE NOTE 1)

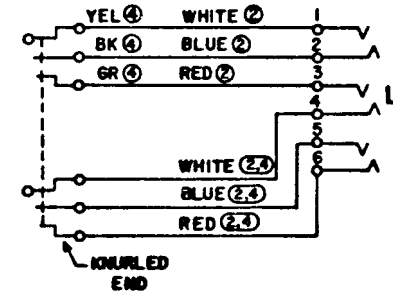
NOTE:
1. LEADS S0-S9 CONNECT TO THE FIRST THRU
TENTH REGISTER SENDERS RESPECTIVELY, ONE
5- LEAD PER REGISTER SENDER.

SD-65827-01-B1

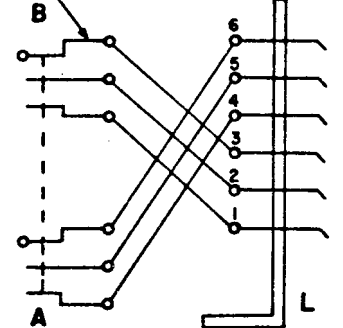
PART OF FS I
TEST CIRCUIT



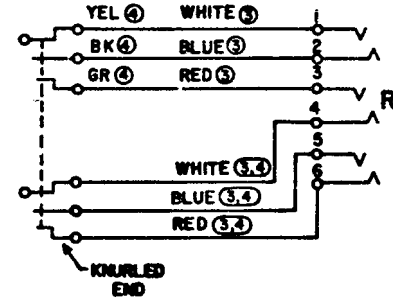
① PATCHING CORD NO. 1
(SEE NOTES 201, 202)



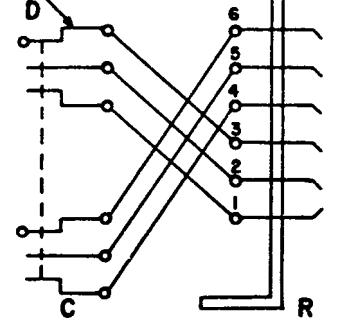
② PATCHING CORD NO. 1
(SEE NOTE 202)



③ PATCHING CORD NO. 2
(SEE NOTES 201, 202)



④ PATCHING CORD NO. 2
(SEE NOTE 202)

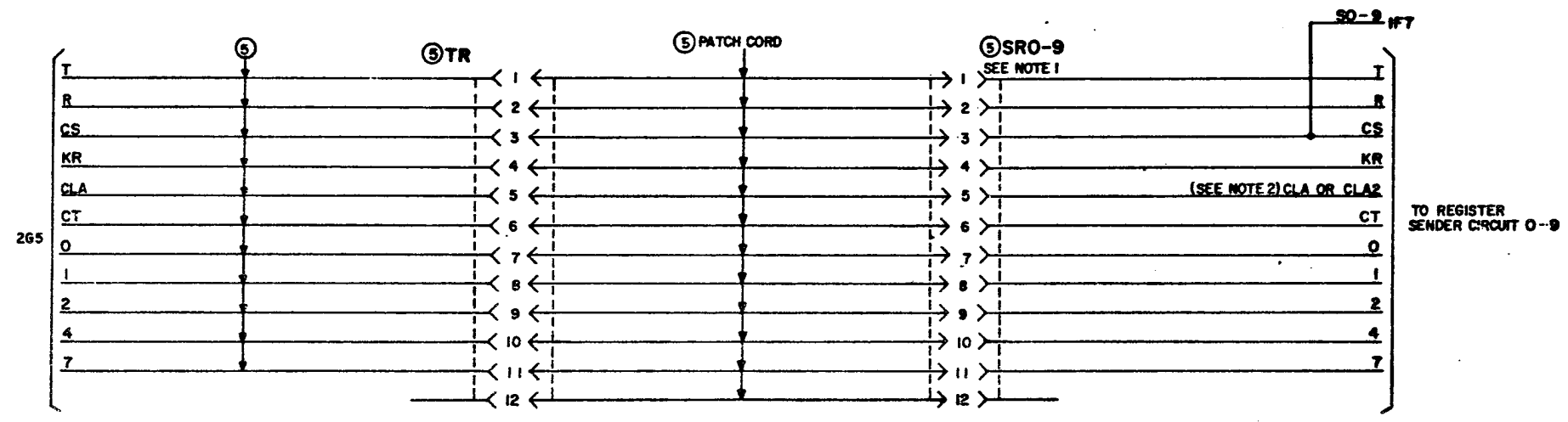


SD-65827-01-B2

DRAWING		E.A. NY
ISSUE	REVISION	
1	JB	NY
28	ES	NY
30	JBS	NY
3D	POB	NY
4D	GDP	NY
5D	JBS	NY
6D	PD	NY
7D	VS	NY
8D	HM	NY
9D	TEB	NY
10D	VOG	NY
11D	GE	NY
12D	TEB	NY
13D	CED	NY
14D	FJS	NY
15D	IRHP	NY
LOST DRAWING REPRODUCED WITHOUT CHANGE 9-17-69 RPR		NY
78		NY

FS2

TEST PATCH CIRCUIT



- NOTES:
1. ONE SR- RECEPTACLE IS PROVIDED PER REGISTER SENDER CIRCUIT.
 2. LEAD DESIGNATION CLA IS ASSOCIATED WITH REGISTER-SENDER CIRCUIT; SD-65816-01, OPTION ZR. LEAD DESIGNATION CLA2 IS ASSOCIATED WITH REGISTER-SENDER CIRCUIT, SD-65816-01, OPTIONS ZS AND YS.

SD-65827-01-83

TEST CIRCUIT	②	SD-65827-01-83
BELL TELEPHONE LABORATORIES	GS	

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APP FIG. 1

APP FIG. 5

RELAY	C		CLA2						P		P1		P2		P3		P4		P5				SC		DESIG
CODE	AG37				AK4				AF527		AF518		AF518		AF515		AF515		AF515				AJ83		CODE
OPTION					K																				OPTION
✕	CONT ARR	LOC	CONT ARR	LOC	CONT ARR	LOC	CONT ARR	LOC	CONT ARR	LOC	CONT ARR	LOC	CONT ARR	LOC	CONT ARR	LOC	CONT ARR	LOC	CONT ARR	LOC	CONT ARR	LOC	CONT ARR	LOC	✕
12			M								EMB	1F3	EMB	1B2	BM	1F2	BM	1F1	BM	1G1			EMB		12
11			EMB	2B3							M	1B3	M	1D3	M	1B2	M	1E2	M	1F2			EMB		11
10	M	1A3	EMB	2C4					M	2E3	EMB	1C7	EMB	1G2	BM	1B8	BM	1B7	BM				EMB		10
9			EMB																				EMB	2C3	9
8	EMB	1G7	EMB	2C3					BM	1G3	EMB	1D2	EMB	1D6	BM		BM	1A8	BM	1D2			EMB		8
7	EMB	1F5									B	1E3	B	1E3	B	1E3	M	1G2	M	1F3			EMB		7
6	EMB	1B3							BM	1A3	EMB	1D7	EMB	1A1	BM	1D7	BM	1D7	BM	1D8			EMB	2D5	6
5																							EMB	2E4	5
4	EMB	1G1							M	1F3	EMB	1C7	EMB	1F2	BM	1D7	BM	1D1	BM	1D2			EMB	2C5	4
3											M		M		M	1D2	M		M				EMB	2B3	3
2											EMB	1B2	EMB	1C2	BM	1G2	BM	1C2	BM	1C3			EMB	2D4	2
1											M	1B8	M		M		M		M				EMB	1G3	1
COIL	✕	1G1	✕	2C3	✕		✕		✕	2A4	✕	1A1	✕	1B1	✕	B1	✕	1D1	✕	1F1	✕		✕	2E3	COIL

NETWORK

DESIG	LOC	CODE
C	1G1	185A
P	2A4	
P1	1A1	
P2	1B1	
P3	1B1	
P4	1D1	
P5	1F1	

CORD

DESIG	LOC	CODE
PATCH CORD	3A4	P12A, 3 FOOT CORD

SOCKET

DESIG	LOC	CODE
SRO-9	3A6	KS-8586, L40
TR	3B3	KS-8586, L40

RESISTOR

OPTION	DESIG	LOC	CODE
A		2C5	18AJ
B		2D3	19JJ
HG		2B5	18HC
P1		1B3	KS-13490, L1, .1 MEG
P2		1B3	
RL		2C3	18CB

APP FIG. 2 (MFR DISC.)

CORD	DESIG	LOC	CODE
[2] -		2B7	P3U
PLUG	DESIG	LOC	CODE
[1] (A)		2B7	253A
(B)		2C7	
L		2B8	351G

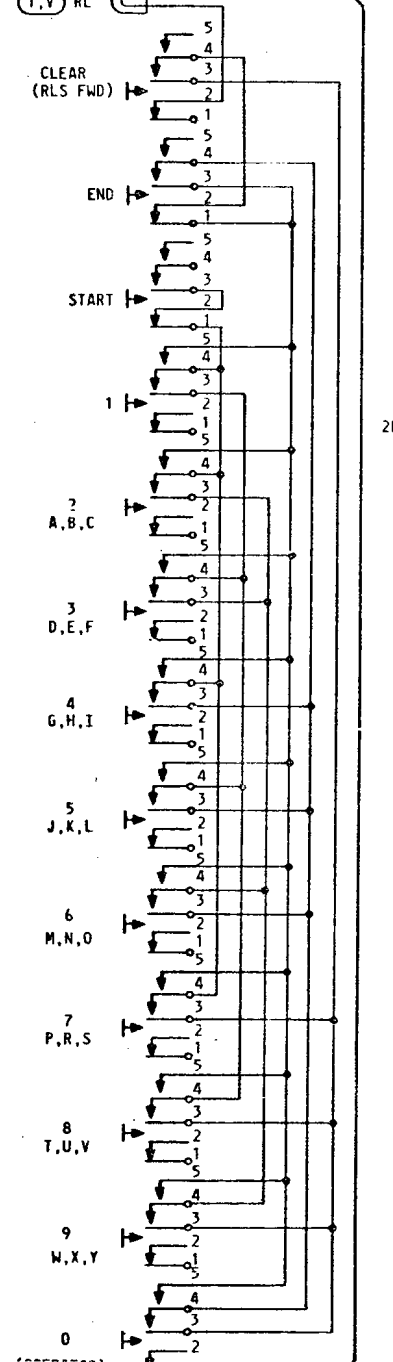
APP FIG. 3 (MFR DISC.)

CORD	DESIG	LOC	CODE
[2] -		2D7	P3U
PLUG	DESIG	LOC	CODE
[1] (C)		2D7	353A
(D)		2E7	
R		2D8	351G

APP FIG. 4

OPTION	DESIG	LOC	CODE
N	PATCH CORD 1	2B9	6P11A
Q	PATCH CORD 1	2B7	P6K
N	PATCH CORD 2	2D9	6P11A
Q	PATCH CORD 2	2D7	P6K
PLUG	DESIG	LOC	CODE
Q	[1] (A)	2B7	428A
(B)		2C7	
Q	[1] (C)	2D7	428A
(D)		2E7	
Q	L	2B8	351G
Q	R	2D8	351G

KEY 702A PUSH BUTTON DIAL



LAMP

OPTION	DESIG	LOC	CODE
CS		2D4	13J (RES)
DIGIT 0		1C6	
1		1A6	
2		1B6	
3		1B6	
4		1B6	
5		1A6	
6		1C6	
7		1D6	
8		1D6	
9		1C6	
RL		2C4	
SDR9		2B0	
1		1G6	
2		1G6	
3		1F6	
4		1F6	
5		1F6	
6		1E6	
7		1E6	
8		1E6	
9		1D6	

LAMP CAP

OPTION	DESIG	CODE
DIGIT 0		72A
1		72B
2		72C
3		72D
4		72E
5		72F
6		72G
7		72H
8		72J
9		72K
DTM		2AY
RL		2H
SDR9		
1		
2		
3		
4		
5		
6		
7		
8		
9		

72P USED IN PLACE OF 72R LAMP CAPS FOR UNEQUIPPED SENDER POSITIONS.

DIODE

DESIG	LOC	CODE
A	1G6	
B	1G6	
C	1G6	
CS	2C3	
D	1F6	
E	1F6	
F	1F6	
G	1E6	
H	1E6	
J	1E6	
K	1D6	

KEY 552A

CT	RV
6	2F4
5	2F4
4	2B5
3	2A5
2	2C5
1	2C5

KEY 92W

CT	RV
6	2A5
5	2A5
4	2A5
3	2A6
2	2A6
1	2A6

JACK

DESIG	LOC	CODE
A	2C6, 2B6	246A
B	2E6, 2D6	
C	2E6, 2D6	
D	2E6, 2D6	
PPS	2A6	239A

KEY 547A

CLA
2C4
2C4

KEY 552E

CMS
6
5
4
3
2
1
2B5
2B5
2B4
2B4

KEY TOP

CMS
HG
CT
OFF
RV
OFF

CIRCUIT NOTES:

DESIG	FUSE AMP	POTENTIAL	ONE PER
1-1/3	-48V SIGNAL	TEST CIRCUIT	
1/2	+10V SIGNAL	TEST CIRCUIT	
1/2	+24V SIGNAL	TEST CIRCUIT	
BATTERY SYMBOL		VOLTAGE RANGE	
-48		44 - 52	
+10		9 - 11	
+24		22 - 26	

FEATURE OR OPTION	PROVIDE		
	APP FIG.	APP OR WRG	QUANTITY
TEST CIRCUIT (SEE NOTE 108)	1,5		ONE PER SDR GRP
WHEN +10V IS AVAILABLE IN PBX		W	
WHEN +24V IS AVAILABLE IN PBX		X	
PATCHING CORD	4		TWO PER APP FIG. 1
WHEN USED WITH 608B OR 608D SWITCHBOARDS		E	
WHEN USED WITH 1 AND 2, OR 21, 41, AND 51 TYPE TELEPHONE CONSOLE		T	

NETWORK VALUES		
NETWORK NO.	RESISTANCE IN OHMS	CAPACITANCE IN UF
1	470	0.11
2	120	0.30

CIRCUIT NOTES: (CONT)

RECORD OF FIGURES, WIRING AND APPARATUS CHANGES						
CHANGED ON ISS	IF JOB RECORDS DO NOT SPECIFY	THIS OPTION WAS FURN	SEE NOTE	USE IN CIRCUIT		
				STD	A&M	MD
2B	Y OR Z	Z	102	Y		Z
	W OR X	W	102	W,X		
3B			102	APP FIG 2,3		
4D	APP FIG. 2,3,4	APP FIG. 2,3	102, 202	4		2,3
5D	T OR V	V	102	T		V
6B	R	S		R		S
	N	Q		N		Q
	K OR M	M		K		M
7B	APP FIG. 5	NONE	102	APP FIG. 5		
	F, G	G		F		G
	E	NONE	102	E		

105. PRIOR TO ISSUE 3D APP FIGS. 2 & 3 WERE PART OF APP FIG. 1.
106. K OPTION SHALL BE FURNISHED FOR USE WITH REG-SDR ISSUE 11B OR LATER.
107. PRIOR TO ISSUE 9B OF REG-SDR THE CLA KEY WAS USED TO CHANGE PULSING SPEED TO 20 PPS WHEN T OPTION WAS FURNISHED IN REG SDR. ISSUE 9B OF REG SDR USED CLA FUNCTION TO PREVENT RECYCLE IN ERROR. CLA KEY HAD NO SPEED CONTROL FUNCTION AFTER THIS ISSUE.
108. THE TEST PATCH CIRCUIT IS FURNISHED AS PART OF THE BASIC REGISTER SENDER TEST UNIT. FOR FIELD MODIFICATIONS TO ADD THE TEST PATCH CIRCUIT TO AN EXISTING REGISTER SENDER TEST UNIT, APP. FIG. 5 CAN BE ORDERED AS SEPARATE A&M RATED EQUIPMENT UNIT PER J58837AD.

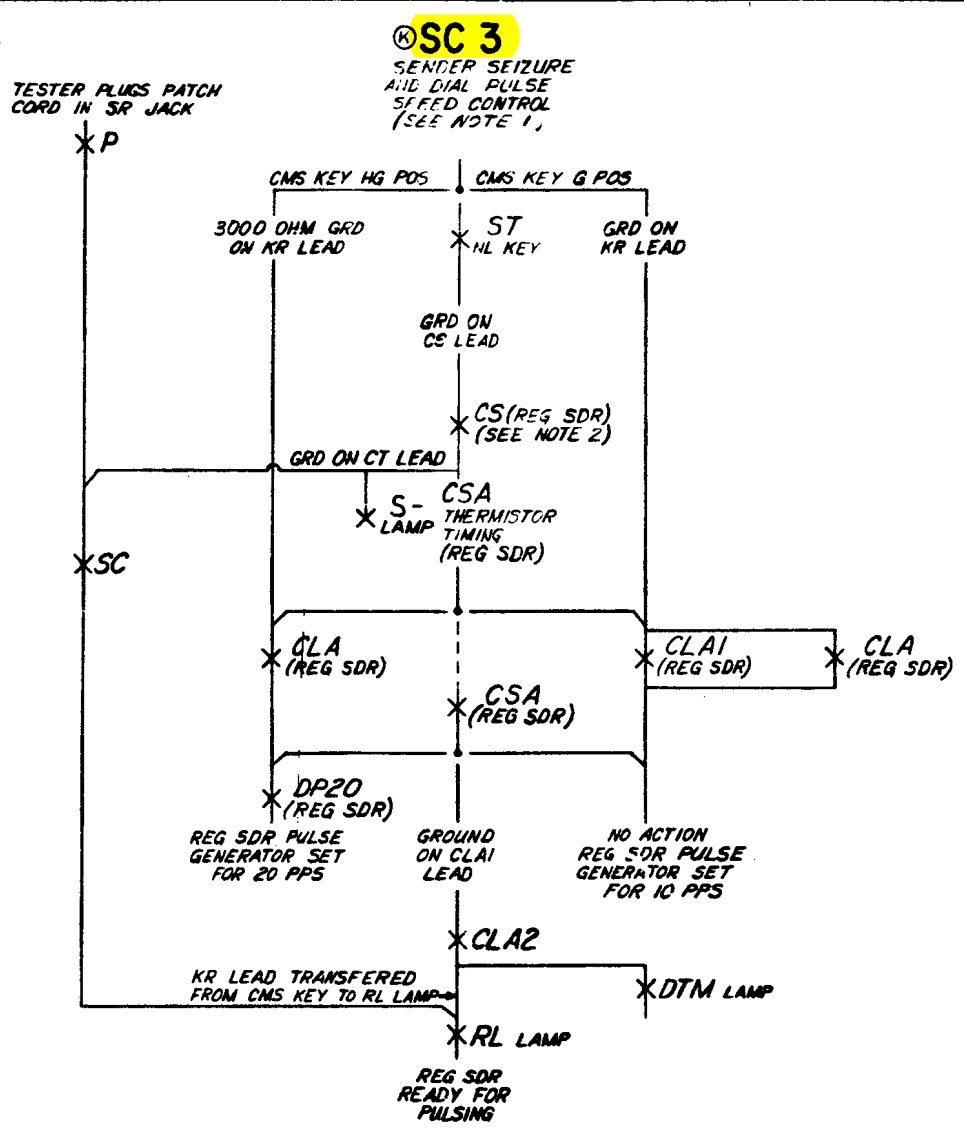
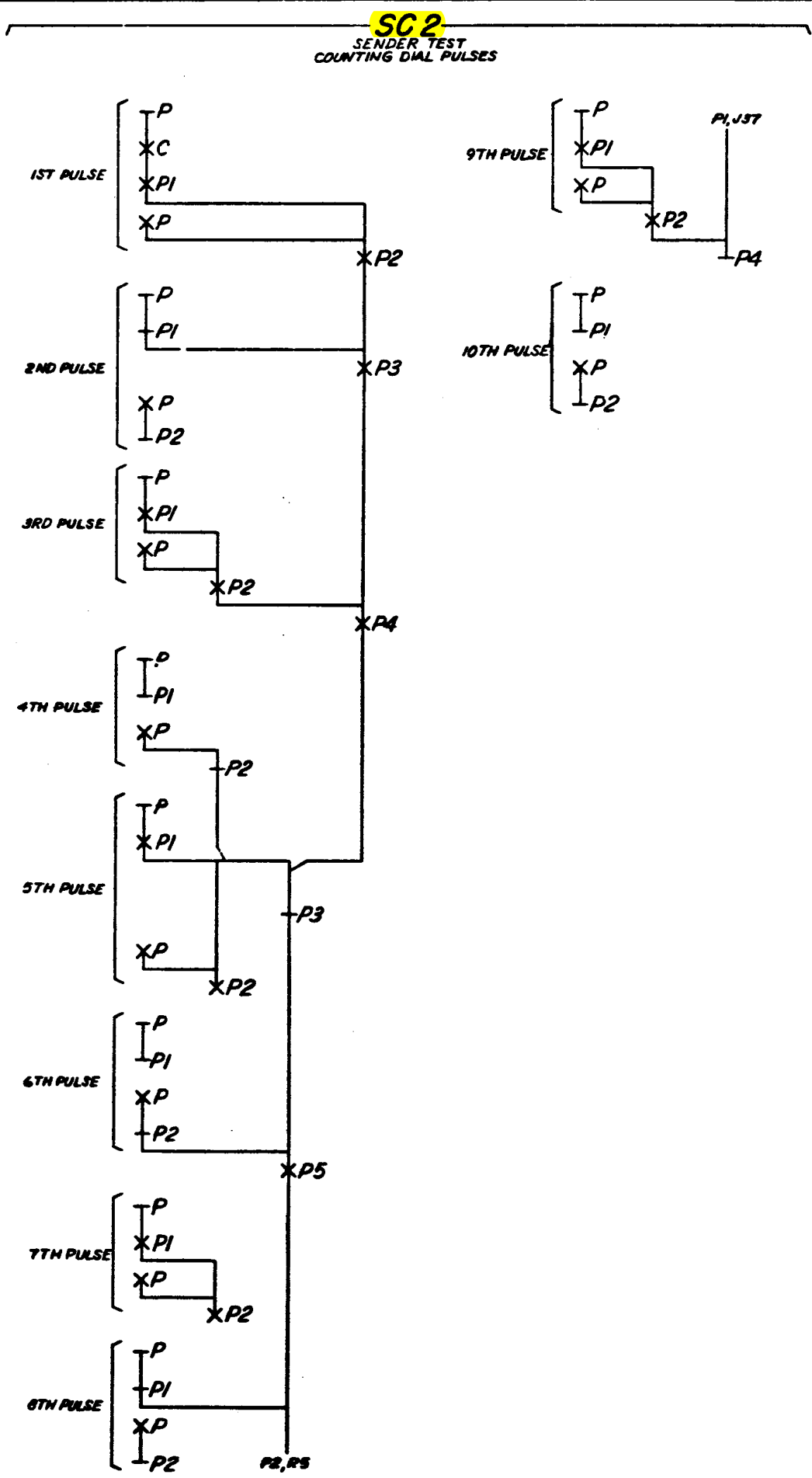
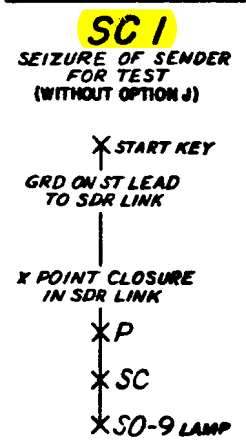
EQUIPMENT NOTES:

201. THE KNURLED PORTION OF ONE 353A PLUG TO BE STENCILED "B". THE OTHER PORTION "A". THE 351G PLUG TO BE STENCILED "L". THE KNURLED PORTION OF THE OTHER 353A PLUG TO BE STENCILED "D", THE OTHER PORTION "C". THE 351G PLUG TO BE STENCILED "R".
202. INSTALLER TO STENCIL PATCHING CORDS 1 & 2 AS INDICATED ON SHEET 3.

INFORMATION NOTES:

301. UNLESS OTHERWISE SPECIFIED:
RESISTANCE VALUES ARE IN OHMS.
CAPACITANCE VALUES ARE IN MICROFARADS.
VALUES PRECEDED BY THE SYMBOL + (PLUS)
OR - (MINUS) ARE IN VOLTS.

DRAWING	ISSUE
1	1
2B	1
30	2
4D	3
5D	4
6B	5
7B	6
7B	7



NOTES:
1. REGISTER SENDER PER SD-65816-01 MUST BE EQUIPPED
WITH YS AND YT OPTIONS (DRAWING ISSUE 11B A&M ONLY).
2. REGISTER SENDER PER SD-65816-02 RELAYS CS AND
CSA RELAYS ARE DESIGNATED ON AND ONI.

SD-65827-01		CIRCUIT REQUIREMENTS															DRAWING	
NO. 7018 REGISTER SENDER AND SENDER LINK TEST CIRCUIT																	ISSUE	
APPARATUS				MECH. REQ.		CIRCUIT PREPARATION				DIRECT CURRENT FLOW REQ.				REMARKS				
DESC	CODE	OPTION	PG	LOC	REF	CON	ARM	BLOCK OR INSULATE	TEST CLIP DATA	TEST SET PREP	SEE TEST NOTE	TEST WDG	TEST FOR				AFTER SOAK	TEST
RELAYS																		
C	AG37		1	2G1	297B				1U(C)	GRD		P	O	FS	48.5	46	SHORT CIRCUIT SEC WDG BY CONNECTING 2L(C) TO 2U(C)	
									1U(C)			P	H	FS	6.3	5.9		
									1U(C)			P	R	FS	3.9	4.6		
									2L (C)	B/G		S	O		60.5			
CLA2	AK4	K	1	2C3	202				1U(CLA2)	GRD			O		11.9	11.3	MOUNTED WITH SPARE	
P	AF527		1	3B4	18				U(P)	GRD			O		36.5	34.5		
P1	AF518		1	2A1	224				U(P1)	GRD			O		100	95		
P2	AF518		1	2B1	224				U(P2)	GRD			O		100	95		
P3	AF515		1	2B1	31				U(P3)	GRD			O		95	90		
P4	AF515		1	2D1	31				U(P4)	GRD			O		95	90		
P5	AF515		1	2F1	31				U(P5)	GRD			O		95	90		
SC	AJ83		1	3E4	249				U(SC)	GRD			O		13.2	12.6		

