ALARM SURVEILLANCE AND CONTROL SYSTEM
INTERCONNECTION CIRCUIT TESTS
NO. 5 CROSSBAR OFFICES

1. GENERAL

1.01 This section describes test procedures for verifying alarm, status, and control leads within No. 5 crossbar offices served by a remote Alarm Surveillance and Control System. These tests may be performed either prior to connection to the Alarm Surveillance and Control center or for trouble detecting purposes in a working configuration.

1.02 This section is reissued for the reasons listed below. Since this is a general revision, revision arrows are not used. This reissue does not affect Equipment Test Lists.

(a) Part 2 Apparatus, is revised to provide optional test apparatus for verifying condition of scan leads.

(b) Part 3, Preparation, is revised to add a step for releasing releasable alarms.

(c) Part 4, Method, is revised to indicate the condition expected on the lead under test.

(d) To make minor changes as required.

1.03 Only those alarm, status, and control leads extended to the interface and control circuit, SD-28075-01, are covered by this section. Special alarm, status and control leads unique to the central office under test and not extended via the interface and control circuit, are not covered.

1.04 Table A contains scan lead tests A through AS for alarm and status conditions. Table B contains scan lead tests BA through BM for control and status conditions. The circuits covered by these tests are as follows:

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COMMAND AND STATUS LEAD TESTS—TABLE B

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BM. Line Concentrator Identifier Circuit (SD-95964-01): 33

1.05 The statement between the asterisks (*.*) after action or verification statements is added to clarify the function being simulated in the test procedures.

2. APPARATUS

All Tests

2.01 KS-14510 L3 volt-ohmmeter, equipped with L3 test probes or POLAR PROBE* CMC 5334A (used to verify that ground is present at the interface and control terminal strip terminals). Do not use test receiver.

*Trademark Communications Manufacturing Co.

2.02 322A (make-busy) plugs as required.

2.03 Blocking and insulating tools as required. Use tools and apply as covered in Section 069-020-801.

2.04 Testing cords as required, 893 cord, 6 feet long, equipped with two 360A tools (1W13B cord), one 419A tool, and one KS-6278 connecting clip (used to connect ground to relay contacts on apparatus side of circuit under test).

2.05 Testing cords as required, 893 cord, 6 feet long, equipped with two 360A tools (1W13B cord), and two KS-6278 connecting clips (used to establish connections to terminal strip terminals of the interface and control circuit).

2.06 Testing cords as required, 893 cord, 6 feet long, equipped with two 360A tools (1W13B cord), one 670A tool, and one KS-6278 connecting
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clip (used to connect ground to non-wire-spring-relay windings on apparatus side of circuit under test).

3. PREPARATION

3.01 If office under test is on-line to a remote alarm surveillance and control center, notify center that local tests are to be made. Indicate what equipment is to be tested, when testing will start, and the estimated time of completion of tests.

3.02 From control center, determine if any remote controls have been applied, eg, equipment made busy, recorder transferred, etc. Apply local controls. At MTF jack, lamp, and key circuit, momentarily operate REL key.

3.03 At the conclusion of testing, notify remote Alarm Surveillance and Control center. Restore all keys and switches to position found at start of test.

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<thead>
<tr>
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<th>ACTION</th>
<th>VERIFICATION</th>
</tr>
</thead>
<tbody>
<tr>
<td>All Tests</td>
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</table>

1. At MTF jack, lamp, and key circuit—Momentarily operate REM key to place office alarms in remote (unattended) mode. REM lamp lighted.

2. At MTF jack, lamp, and key circuit—Momentarily operate MAR key.
   *Momentary operation of the MAR key will release a locked in alarm, provided the trouble condition causing the alarm has been corrected.*

3a. If any control function has been initiated at control center—At MTF, jack, lamp, and key circuit—Momentarily operate REL key.
   
   **Note:** If CAL lamp lighted—control center requests a telephone call from the local office. Contact control center for instruction.

   CTL lamp extinguished.
### 4. METHOD

**SCAN LEAD TESTS—TABLE A**

**A. Alarm Circuit (SD-25671-01)**

<table>
<thead>
<tr>
<th>STEP</th>
<th>ACTION</th>
<th>VERIFICATION</th>
</tr>
</thead>
</table>
| 4    | At interface and control circuit—
      | Connect test probe as required, to terminal strip terminal as specified in Table A, Test A. | If an alarm is locked-in to scan lead under test—
      | Ground present at terminal strip terminal. | |
| 5b   | If an alarm is locked-in to scan lead under test—
      | At MTF jack, lamp, and key circuit—
      | Momentarily operate MAR key. | At interface and control circuit—
      | Ground removed from terminal strip terminal. | |
| 6    | At alarm circuit under test—
      | Block operated relay associated with scan lead under test as specified in Table A, Test A. | At interface and control circuit—
      | Ground present at terminal strip terminal. | |
| 7    | At alarm circuit under test—
      | Remove blocking tool placed in Step 6. | |
| 8    | At interface and control circuit—
      | Disconnect test probe from terminal strip terminal. | |
| 9    | Repeat Steps 4 through 8 as required for each scan lead under test as specified in Table A, Test A. | |

**B. Master Test Frame Jack, Lamp, and Key Circuit (SD-25762-01)**

<table>
<thead>
<tr>
<th>STEP</th>
<th>ACTION</th>
<th>VERIFICATION</th>
</tr>
</thead>
</table>
| 4    | At interface and control circuit—
      | Connect test probe as required, to terminal strip terminal as specified in Table A, Test B. | If an alarm is locked-in to scan lead under test—
      | Ground present at terminal strip terminal. | |
| 5b   | If an alarm is locked in to scan lead under test—
      | At MTF jack, lamp, and key circuit—
      | Momentarily operate MAR key. | At interface and control circuit—
      | Ground removed from terminal strip terminal. | |
| 6    | At circuit under test—
      | Block operated relay associated with scan lead under test as specified in Table A, Test B. | At interface and control circuit—
      | Ground present at terminal strip terminal. | |
| 7    | At circuit under test—
      | Remove blocking tool placed in Step 6. | |
8 At interface and control circuit—
Disconnect test probe from terminal strip
terminal.

9 Repeat Steps 4 through 8 as required for each
scan lead under test as specified in Table A,
Test B.

C. Combined/Completing Marker Circuit (SD-25550-01/
SD-26002-01) First Trial Failure

4 At interface and control circuit—
Connect test probe as required, to terminal
strip terminal as specified in Table A, Test
C.

5b If an alarm is locked-in to scan lead under
test—
At jack, lamp, and key circuit—
Momentarily operate MAR key.

6 At marker circuit under test—
Connect ground to relay contact associated
with scan lead under test as specified in Table
A, Test C.

7 At marker circuit under test—
Disconnect ground placed in Step 6.

8 At interface and control circuit—
Disconnect test probe from terminal strip
terminal.

9 Repeat Steps 5 through 8 for each scan lead
under test as specified in Table A, Test C,
for each marker circuit.

D. Combined/Completing Marker Circuit (SD-25550-01/
SD-26002-01) Second Trial Failure

4 At interface and control circuit—
Connect test probe as required to terminal
strip terminal as specified in Table A, Test
D.

5b If an alarm is locked-in to scan lead under
test—
At jack, lamp, and key circuit—
Momentarily operate MAR key.

6 At marker circuit under test—
Connect ground to relay contact associated
with scan lead under test as specified in Table
A, Test D.

If an alarm is locked-in to scan lead under
test—
Ground present at terminal strip terminal.

At interface and control circuit—
Ground removed from terminal strip terminal.

At interface and control circuit—
Ground present at terminal strip terminal.

Single digit 2nd trial failure register for
combined/completing markers advances one
registration.
7 At marker circuit under test—
   Disconnect ground placed in Step 6.

8 At interface and control circuit—
   Disconnect test probe from terminal strip terminal.

9 Repeat Steps 4 through 8 for each lead under test as specified in Table A, Test D, for each marker circuit.

E. Combined/Completing Marker Circuit (SD-25550-01/SD-26002-01) Link Release

4 At interface and control circuit—
   Connect test probe as required, to terminal strip terminal as specified in Table A, Test E.

5b If an alarm is locked-in to scan lead under test—
   At jack, lamp, and key circuit—
   Momentarily operate MAR key.

6 At marker circuit under test—
   Connect ground to relay contact associated with scan lead under test as specified in Table A, Test E.

7 At marker circuit under test—
   Disconnect ground placed in Step 6.

8 At interface and control circuit—
   Disconnect test probe from terminal strip terminal.

9 Repeat Steps 4 through 8 for each scan lead under test as specified in Table A, Test E, for each marker circuit.

F. Combined/Completing Marker Circuit (SD-25550-01 or SD-26002-01) Transfer Link Release

4 At interface and control circuit—
   Connect test probe as required to terminal strip terminal as specified in Table A, Test F.

5b If an alarm is locked-in to scan lead under test—
   At jack, lamp, and key circuit—
   Momentarily operate MAR key.

If an alarm is locked-in to scan lead under test—
   Ground present at terminal strip terminal.

At interface and control circuit—
   Ground removed from terminal strip terminal.

At interface and control circuit—
   Ground present at terminal strip terminal.

At interface and control circuit—
   Ground present at terminal strip terminal.
6. At marker circuit under test—
Connect ground to relay contact associated with scan lead under test as specified in Table A, Test F.

7. At marker circuit under test—
Disconnect ground placed in Step 6.

8. At interface and control circuit—
Disconnect test probe from terminal strip terminal.

9. Repeat Steps 4 through 8 for each scan lead under test as specified in Table A, Test F, for each marker circuit.

G. Dial Tone Marker Circuit (SD-25550-01/SD-26001-01) First Trial Failure

4. At interface and control circuit—
Connect test probe as required to terminal strip terminal as specified in Table A, Test G.

5b. If an alarm is locked-in to scan lead under test—
At jack, lamp and key circuit—
Momentarily operate MAR key.

6. At marker circuit under test—
Connect ground to relay contact associated with scan lead under test as specified in Table A, Test G.

7. At marker circuit under test—
Disconnect ground placed in Step 6.

8. At interface and control circuit—
Disconnect test probe from terminal strip terminal.

9. Repeat Steps 4 through 8 for each scan lead under test as specified in Table A, Test G, for each marker circuit.

H. Dial Tone Marker Circuit (SD-25550-01/SD-26001-01) Second Trial Failure

4. At interface and control circuit—
Connect test probe as required, to terminal strip terminal as specified in Table A, Test H.

If an alarm is locked-in to scan lead under test—
Ground present at terminal strip terminal.

At interface and control circuit—
Ground removed from terminal strip terminal.

At interface and control circuit—
Ground present at terminal strip terminal.

At marker circuit under test—
Connect ground to relay contact associated with scan lead under test as specified in Table A, Test G.

At interface and control circuit—
Ground present at terminal strip terminal.
If alarm is locked-in to scan lead under test—
At jack, lamp, and key circuit—
Momentarily operate MAR key.

At marker circuit under test—
Connect ground to relay contact associated with scan lead under test as specified in Table A, Test H.

At marker circuit under test—
Disconnect ground placed in Step 6.

At interface and control circuit—
Disconnect test probe from terminal strip terminal.

Repeat Steps 4 through 8 for each scan lead under test as specified in Table A, Test H, for each marker circuit.

1. **Transverter Circuit** (SD-25591-01/SD-26010-01, SD-26161-01/SD-27809-01 or SD-28085-01) First Trial Failure

At interface and control circuit—
Connect test probe as required, to terminal strip terminal as specified in Table A, Test I.

If an alarm is locked-in to scan lead under test—
Ground present at terminal strip terminal.

At interface and control circuit—
Ground removed from terminal strip terminal.

At transverter circuit under test—
Connect ground to relay contact associated with scan lead under test as specified in Table A, Test I.

At transverter circuit under test—
Disconnect ground placed in Step 6.

At interface and control circuit—
Disconnect test probe from terminal strip terminal.

Repeat Steps 4 through 8 for each scan lead under test as specified in Table A, Test I, for each Transverter.
J. Transverter Circuit (SD-25591-01/SD-26010-01, or SD-26161-01/SD-27809-01 or SD-28085-01)  
Second Trial Failure

4. At interface and control circuit—
   Connect test probe as required, to terminal strip terminal as specified in Table A, Test J.

5b. If an alarm is locked-in to scan lead under test—
    At jack, lamp, and key circuit—
    Momentarily operate MAR key.

6. At transverter circuit under test—
   Connect ground to relay contact associated with scan lead under test as specified in Table A, Test J.

7. At transverter circuit under test—
   Disconnect ground placed in Step 6.

8. At interface and control circuit—
   Disconnect test probe from terminal strip terminal.

9. Repeat Steps 4 through 8 for each scan lead under test as specified in Table A, Test J.

K. Pretranslator Circuit (SD-25568-01) First Trial Failure

4. At interface and control circuit—
   Connect test probe as required, to terminal strip terminal as specified in Table A, Test K.

5b. If an alarm is locked-in to scan lead under test—
    At jack, lamp, and key circuit—
    Momentarily operate MAR key.

6. At pretranslator circuit under test—
   Connect ground to relay contact associated with scan lead under test as specified in Table A, Test K.

7. At pretranslator circuit under test—
   Disconnect ground placed in Step 6.

8. At interface and control circuit—
   Disconnect test probe from terminal strip terminal.

If an alarm is locked-in to scan lead under test—
Ground present at terminal strip terminal.

At interface and control circuit—
Ground removed from terminal strip terminal.

At transverter circuit under test—
Ground present at terminal strip terminal.
Single digit 2nd trial register for transverters advances one registration.

If an alarm is locked-in to scan lead under test—
Ground present at terminal strip terminal.

At interface and control circuit—
Ground removed from terminal strip terminal.

At interface and control circuit—
Ground present at terminal strip terminal.
Repeat Steps 4 through 8 for each scan lead under test as specified in Table A, Test K, for each pretranslator.

L. Pretranslator Circuit (SD-25568-01) Second Trial Failure

4 At interface and control circuit—
   Connect test probe as required, to terminal strip terminal as specified in Table A, Test L.

5b If an alarm is locked-in to scan lead under test—
   At jack, lamp, and key circuit—
   Momentarily operate MAR key.

6 At pretranslator circuit under test—
   Connect ground to relay contact associated with scan lead under test as specified in Table A, Test L.

7 At pretranslator circuit under test—
   Disconnect ground placed in Step 6.

8 At interface and control circuit—
   Disconnect test probe from terminal strip terminal.

9 Repeat Steps 4 through 8 for each scan lead under test as specified in Table A, Test L, for each pretranslator.

M. Trouble Recorder Control and Test Circuit (SD-25572-01)

4 At interface and control circuit—
   Connect test probe as required, to terminal strip terminal as specified in Table A, Test M.

5b If an alarm is locked-in to scan lead under test—
   At jack, lamp, and key circuit—
   Momentarily operate MAR key.

6 At trouble recorder control circuit under test—
   Connect ground to relay contact or block operated relay associated with scan lead under test as specified in Table A, Test M.

7 At trouble recorder control circuit—
   Disconnect ground or remove blocking tool placed in Step 6.
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8 At interface and control circuit—
Disconnect test probe from terminal strip terminal.

9 Repeat Steps 4 through 8 for each scan lead under test as specified in Table A, Test M.

N. Coin Supervisory Circuit (SD-25736-01)

4 At interface and control circuit—
Connect test probe as required, to terminal strip terminal as specified in Table A, Test N.

5b If an alarm is locked-in to scan lead under test—
At jack, lamp, and key circuit—
Momentarily operate MAR key.

6 At coin supervisory circuit under test—
Connect ground to relay contact or block operated relay associated with scan lead under test as specified in Table A, Test N.

7 At coin supervisory circuit under test—
Disconnect ground or remove blocking tool placed in Step 6.

8 At interface control circuit—
Disconnect test probe from terminal strip terminal.

9 Repeat Steps 4 through 8 for each scan lead under test as specified in Table A, Test N.

O. Automatic Monitor, Register, Sender Test Circuit (SD-25680-01)

4 At interface and control circuit—
Connect test probe as required, to terminal strip terminal as specified in Table A, Test O.

5b If an alarm is locked-in to scan lead under test—
At jack, lamp, and key circuit—
Momentarily operate MAR key.

6 At automatic monitor, register, sender circuit under test—
Connect ground to relay contact or block operated relay associated with scan lead under test as specified in Table A, Test N.

If an alarm is locked-in to scan lead under test—
Ground present at terminal strip terminal.

At interface and control circuit—
Ground removed from terminal strip terminal.

At interface and control circuit—
Ground present at terminal strip terminal.

At interface and control circuit—
Ground present at terminal strip terminal.

At interface and control circuit—
Ground removed from terminal strip terminal.

At interface and control circuit—
Ground present at terminal strip terminal.
7 At automatic monitor, register, sender circuit under test—
Disconnect ground or remove blocking tool placed in Step 6.

8 At interface and control circuit—
Disconnect test probe from terminal strip terminal.

P. Line Insulation Test Control Circuit (SD-25796-01)

4 At interface and control circuit—
Connect test probe as required, to terminal strip terminal as specified in Table A, Test P.

5b If an alarm is locked-in to scan lead under test—
At jack, lamp, and key circuit—
Momentarily operate MAR key.

6 At line insulation test control circuit under test—
Connect ground or block operated relay associated with scan lead under test as specified in Table A, Test P.

7 At line insulation test control circuit under test—
Disconnect ground or remove blocking tool placed in Step 6.

8 At interface and control circuit—
Disconnect test probe from terminal strip terminal.

Q. Dial Tone Speed Register Circuit (SD-96403-01)

4 At interface and control circuit—
Connect test probe as required, to terminal strip terminal as specified in Table A, Test Q.

5b If an alarm is locked-in to scan lead under test—
At jack, lamp, and key circuit—
Momentarily operate MAR key.

6 At dial tone speed register circuit under test—
Connect ground or block operated relay associated with scan lead under test as specified in Table A, Test Q.
At dial tone speed register circuit under test—
Disconnect ground or remove blocking tool placed in Step 6.

At interface and control circuit—
Disconnect test probe from terminal strip terminal.

R. 4A Traffic Usage Recorder Circuit (SD-95738-01)

At interface and control circuit—
Connect test probe as required, to terminal strip terminal as specified in Table A, Test R. If an alarm is locked-in to scan lead under test—
Ground present at terminal strip terminal.

If an alarm is locked-in to scan lead under test—
At jack, lamp, and key circuit—
Momentarily operate MAR key.

At interface and control circuit—
Ground removed from terminal strip terminal.

At 4A traffic usage recorder circuit under test—
Connect ground or block operated relay associated with scan lead under test as specified in Table A, Test R.

Ground present at terminal strip terminal.

At 4A traffic usage recorder circuit under test—
Disconnect ground or remove blocking tool placed in Step 6.

At interface and control circuit—
Disconnect test probe from terminal strip terminal.

S. Maintenance Data Transmitter Circuit (SD-28111-01)

At interface and control circuit—
Connect test probe as required, to terminal strip terminal as specified in Table A, Test S. If an alarm is locked-in to scan lead under test—
Ground present at terminal strip terminal.

If an alarm is locked-in to scan lead under test—
At jack, lamp, and key circuit—
Momentarily operate MAR key.

At interface and control circuit—
Ground removed from terminal strip terminal.

At maintenance data transmitter circuit under test—
Connect ground or block operated relay associated with scan lead under test as specified in Table A, Test S. Ground present at terminal strip terminal.
7 At maintenance data transmitter circuit under test—
  Disconnect ground or remove blocking tool placed in Step 6.

8 At interface and control circuit—
  Disconnect test probe from terminal strip terminal.

T. **Line Load Control Circuit** (SD-96387-01)

4 At interface and control circuit—
  Connect test probe as required, to terminal strip terminal as specified in Table A, Test T.

5b If an alarm is locked-in to scan lead under test—
  At jack, lamp, and key circuit—
  Momentarily operate MAR key.

6 At line load control circuit under test—
  Connect ground or block operated relay associated with scan lead under test as specified in Table A, Test T.

7 At line load control circuit under test—
  Disconnect ground or remove blocking tool placed in Step 6.

8 At interface and control circuit—
  Disconnect test probe from terminal strip terminal.

U. **MF Current Supply Circuit**—(SD-95391-01)

4 At interface and control circuit—
  Connect test probe as required, to terminal strip terminal as specified in Table A, Test U.

5b If an alarm is locked-in to scan lead under test—
  At jack, lamp, and key circuit—
  Momentarily operate MAR key.

6 At MF current supply circuit under test—
  Connect ground or block operated relay associated with scan lead under test as specified in Table A, Test U.

7 At MF current supply circuit under test—
  Disconnect ground or remove blocking tool placed in Step 6.
At interface and control circuit—
Disconnect test probe from terminal strip terminal.

V. Message Register Power Supply Circuit (SD-25770-01)

At interface and control circuit—
Connect test probe as required, to terminal strip terminal as specified in Table A, Test V.

If an alarm is locked-in to scan lead under test—
At jack, lamp, and key circuit—
Momentarily operate MAR key.

Connect ground or block operated relay associated with scan lead under test as specified in Table A, Test V.

At message register power supply circuit under test—
Connect ground or remove blocking tool placed in Step 6.

At interface and control circuit—
Ground present at terminal strip terminal.

W. Line Identifier Power Supply Circuit (SD-27700-01)

At interface and control circuit—
Connect test probe as required, to terminal strip terminal as specified in Table A, Test W.

If an alarm is locked-in to scan lead under test—
Ground present at terminal strip terminal.

At interface and control circuit—
Ground removed from terminal strip terminal.

If an alarm is locked-in to scan lead under test—
Ground present at terminal strip terminal.

At line identifier power supply circuit under test—
Connect ground or block operated relay associated with scan lead under test as specified in Table A, Test W.

At interface and control circuit—
Ground present at terminal strip terminal.

Connect ground or remove blocking tool placed in Step 6.
8  At interface and control circuit—
   Disconnect test probe from terminal strip
   terminal.

X. Extension Alarm Circuit (SD-95484-01)

4  At interface and control circuit—
   Connect test probe as required, to terminal
   strip terminal as specified in Table A, Test
   X.

5b  If an alarm is locked-in to scan lead under
    test—
    At jack, lamp, and key circuit—
    Momentarily operate MAR key.

6  At extension alarm circuit under test—
   Connect ground or block operated relay
   associated with scan lead under test as specified
   in Table A, Test X.

7  At extension alarm circuit under test—
   Disconnect ground or remove blocking tool
   placed in Step 6.

8  At interface and control circuit—
   Disconnect test probe from terminal strip
   terminal.

9  Repeat Steps 4 through 8 for each scan lead
   under test as specified in Table A, Test X.

Y. PBX-AIOD Miscellaneous Circuit (SD-1C006-01)

4  At interface and control circuit—
   Connect test probe as required, to terminal
   strip terminal as specified in Table A, Test
   Y.

5b  If an alarm is locked-in to scan lead under
    test—
    At jack, lamp, and key circuit—
    Momentarily operate MAR key.

6  At PBX-AIOD circuit under test—
   Connect ground or block operated relay
   associated with scan lead under test as specified
   in Table A, Test Y.

7  At PBX-AIOD circuit under test—
   Disconnect ground or remove blocking tool
   placed in Step 6.

   If an alarm is locked-in to scan lead under
   test—
   Ground present at terminal strip terminal.

   At interface and control circuit—
   Ground removed from terminal strip terminal.

   At interface and control circuit—
   Ground present at terminal strip terminal.

   If an alarm is locked-in to scan lead under
   test—
   Ground present at terminal strip terminal.

   At interface and control circuit—
   Ground removed from terminal strip terminal.
8 At interface and control circuit—
   Disconnect test probe from terminal strip terminal.

9 Repeat Steps 4 through 8 for each scan lead under test as specified in Table A, Test Y.

Z. 1A Line Concentrator Circuit (SD-96536-01)

4 At interface and control circuit—
   Connect test probe as required, to terminal strip terminal as specified in Table A, Test Z.

  If an alarm is locked-in to scan lead under test—
   Ground present at terminal strip terminal.

5b If an alarm is locked-in to scan lead under test—
   At jack, lamp, and key circuit—
   Momentarily operate MAR key.

At interface and control circuit—
   Ground removed from terminal strip terminal.

6 At 1A line concentrator under test—
   Connect ground or block operated relay associated with scan lead under test as specified in Table A, Test Z.

At interface and control circuit—
   Ground present at terminal strip terminal.

7 At 1A line concentrator under test—
   Disconnect ground or remove blocking tool placed in Step 6.

8 At interface and control circuit—
   Disconnect test probe from terminal strip terminal.

9 Repeat Steps 4 through 8 for each scan lead under test as specified in Table A, Test Z.

AA. 2A Line Concentrator Circuit—(SD-94815-01)

4 At interface and control circuit—
   Connect test probe as required, to terminal strip terminal as specified in Table A, Test AA.

If an alarm is locked-in to scan lead under test—
   Ground present at terminal strip terminal.

5b If an alarm is locked-in to scan lead under test—
   At jack, lamp, and key circuit—
   Momentarily operate MAR key.

At interface and control circuit—
   Ground removed from terminal strip terminal.

6 At 2A line concentrator circuit under test—
   Connect ground or block operated relay associated with scan lead under test as specified in Table A, Test AA.

At interface and control circuit—
   Ground present at terminal strip terminal.
7 At 2A line concentrator circuit under test—
Disconnect ground or remove blocking tool
placed in Step 6.

8 At interface and control circuit—
Disconnect test probe from terminal strip
terminal.

9 Repeat Steps 4 through 8 for each scan lead
under test as specified in Table A, Test AA.

**AB. All Markers/Transverters Busy Circuit (SD-25695-01)**

4 At interface and control circuit—
Connect test probe as required, to terminal
strip terminal as specified in Table A, Test
AB.

5b If an alarm is locked-in to scan lead under
test—
At jack, lamp, and key circuit—
Momentarily operate MAR key.

6 At marker/transverter busy circuit under
test—
Connect ground or block operated relay
associated with scan lead under test as specified
in Table A, Test AB.

7 At marker/transverter busy circuit under
test—
Disconnect ground or remove blocking tool
placed in Step 6.

7 At interface and control circuit—
Disconnect test probe from terminal strip
terminal.

9 Repeat Steps 4 through 8 for each scan lead
under test as specified in Table A, Test AB.

**AC. Traffic Register Circuit (SD-25890-01/SD-25892-01)**

4 At interface and control circuit—
Connect test probe as required, to terminal
strip terminal as specified in Table A, Test
AC.

5b If an alarm is locked-in to scan lead under
test—
At jack, lamp, and key circuit—
Momentarily operate MAR key.
6 At traffic register circuit under test—
Connect ground or block operated relay associated with scan lead under test as specified in Table A, Test AC.

7 At traffic register circuit under test—
Disconnect ground or remove blocking tool placed in Step 6.

8 At interface and control circuit—
Disconnect test probe from terminal strip terminal.

9 Repeat Steps 4 through 8 for each scan lead under test as specified in Table A, Test AC.

AD. Sender Group Busy Circuit (SD-25500-01/SD-27638-01, SD-25890-01)

4 At interface and control circuit—
Connect test probe as required, to terminal strip terminal as specified in Table A, Test AD.

5b If an alarm is locked-in to scan lead under test—
At jack, lamp, and key circuit—
Momentarily operate MAR key.

6 At sender group busy circuit under test—
Connect ground or block operated relay associated with scan lead under test as specified in Table A, Test AD.

7 At sender group busy circuit under test—
Disconnect ground or remove blocking tool placed in Step 6.

8 At interface and control circuit—
Disconnect test probe from terminal strip terminal.

9 Repeat Steps 4 through 8 for each scan lead under test as specified in Table A, Test AD.

AE. 101 ESS Maintenance Center (SD-1H052-01)

4 At interface control circuit—
Connect test probe as required, to terminal strip terminal as specified in Table A, Test AE.

5b If an alarm is locked-in to scan lead under test—
At jack, lamp, and key circuit—
Momentarily operate MAR key.

If an alarm is locked-in to scan lead under test—
Ground present at terminal strip terminal.

At interface and control circuit—
Ground removed from terminal strip terminal.

At interface and control circuit—
Ground present at terminal strip terminal.

At interface and control circuit—
Ground present at terminal strip terminal.

At interface and control circuit—
Ground removed from terminal strip terminal.
6 At 101 ESS maintenance center control unit under test—
   Connect ground or block operated relay associated with scan lead under test as specified in Table A, Test AE.

7 At 101 ESS maintenance center control unit under test—
   Disconnect ground or remove blocking tool placed in Step 6.

8 At interface and control circuit—
   Disconnect test probe from terminal strip terminal.

9 Repeat Steps 4 through 8 for each scan lead under test as specified in Table A, Test AE.

AF. 3A Auxiliary Processor—ETS (SD-28118-01)

4 At interface and control circuit—
   Connect test probe as required, to terminal strip terminal as specified in Table A, Test AF.

5 If an alarm is locked-in to scan lead under test—
   At system status panel—
   Momentarily operate AR key.

6 At 3A auxiliary processor circuit under test—
   Connect ground or block operated relay associated with scan lead under test as specified in Table A, Test AF.

7 At 3A auxiliary processor circuit under test—
   Disconnect ground or remove blocking tool placed in Step 6.

8 At interface and control circuit—
   Disconnect test probe from terminal strip terminal.

9 Repeat Steps 4 through 8 for each scan lead under test as specified in Table A, Test AF.

AG. Power Distribution Interface Circuit ETS (SD-28114-01) Watch Dog Timer Failure

4 At interface and control circuit—
   Connect test probe as required, to terminal strip terminal as specified in Table A, Test AG.

If an alarm is locked-in to scan lead under test—
   Ground present at terminal strip terminal.

If an alarm is locked-in to scan lead under test—
   Ground present at terminal strip terminal.

At interface and control circuit—
   Ground removed from terminal strip terminal.

At Interface and control circuit—
   Ground present at terminal strip terminal.
If an alarm is locked-in to scan lead under test—
At jack, lamp, and key circuit—
Momentarily operate MAR key.

At power distribution interface circuit under test—
Connect ground or block operated relay associated with scan lead under test as specified in Table A, Test AG.

At power distribution interface circuit under test—
Disconnect ground or remove blocking tool placed in Step 6.

At interface and control circuit—
Disconnect test probe from terminal strip terminal.

Repeat Steps 4 through 8 for each scan lead under test as specified in Table A, Test AG.

If an alarm is locked-in to scan lead under test—
Ground removed from terminal strip terminal.

At interface and control circuit—
Ground present at terminal strip terminal.

At interface and control circuit—
Ground present at terminal strip terminal.

At interface and control circuit—
Ground removed from terminal strip terminal.

At interface and control circuit—
Ground present at terminal strip terminal.
**AI. Program Controlled Transverter Circuit**
*(SD-28085-01)*

4. At interface and control circuit—
   Connect test probe as required, to terminal strip terminal as specified in Table A, Test AI.

5b. If an alarm is locked-in to scan lead under test—
   At jack, lamp, and key circuit—
   Momentarily operate MAR key.

6. At program controlled transverter circuit under test—
   Connect ground or block operated relay associated with scan lead under test as specified in Table A, Test AI.

7. At program controlled transverter circuit under test—
   Disconnect ground or remove blocking tool placed in Step 6.

8. At interface and control circuit—
   Disconnect test probe from terminal strip terminal.

9. Repeat Steps 4 through 8 for each scan lead under Test as specified in Table A, Test AI.

**AJ. Permanent Signal Alarm Circuit** *(SD-26135-01)*

4. At interface and control circuit—
   Connect test probe as required, to terminal strip terminal as specified in Table A, Test AJ.

5b. If an alarm is locked-in to scan lead under test—
   At jack, lamp, and key circuit—
   Momentarily operate MAR key.

6. At permanent signal alarm circuit under test—
   Connect ground or block operated relay associated with scan lead under test as specified in Table A, Test AJ.

7. At permanent signal alarm circuit under test—
   Disconnect ground or remove blocking tool placed in Step 6.

8. At interface and control circuit—
   Disconnect test probe from terminal strip terminal.

If an alarm is locked-in to scan lead under test—
   Ground present at terminal strip terminal.

At interface and control circuit—
   Ground removed from terminal strip terminal.

If an alarm is locked in to scan lead under test—
   Ground present at terminal strip terminal.

At interface and control circuit—
   Ground removed from terminal strip terminal.
9 Repeat Steps 4 through 8 for each scan lead under test as specified in Table A, Test AJ.

AK. Master Test Frame Jack, Lamp, and Key Circuit (SD-25762-01)

4 At interface and control circuit—
Connect test probe as required, to terminal strip terminal as specified in Table A, Test AK.

5b If an alarm is locked-in to scan lead under test—
At jack, lamp, and key circuit—
Momentarily operate MAR key.

If an alarm is locked-in to scan lead test—
Ground present at terminal strip termina.

At interface and control circuit—
Ground removed from terminal strip terminal.

6 Operate key associated with scan lead under test as specified in Table A, Test AK.

At interface and control circuit—
Ground present at terminal strip terminal.

7 At master test frame jack, lamp, and key circuit—
Restore keys to position for normal attended operation.

8 At interface and control circuit—
Disconnect test probe from terminal strip terminal.

9 Repeat Steps 4 through 8 for each scan lead under Test as specified in Table A, Test AK.

AL. Alarm Sending Circuit (SD-95417-01)

4 At interface and control circuit—
Connect test probe as required, to terminal strip terminal as specified in Table A, Test AL.

5 At MTF, jack, lamp, and key circuit—
Operate alarm sending transfer key to transfer position.

At MTF, jack, lamp, and key circuit—
Ground present at terminal strip terminal.

6 At MTF, jack, lamp, and key circuit—
Restore alarm sending transfer to non-transferred position. Momentarily operate RS key.

At interface and control circuit—
Ground removed from terminal strip terminal.

7 At interface and control circuit—
Disconnect test probe from terminal strip terminal.

AM. AMA Recorder Transfer Circuit (SD-28076-01)

4 At interface and control circuit—
Connect test probe as required, to terminal strip terminal as specified in Table A, Test AM.

If an alarm is locked-in to scan lead under test—
Ground present at terminal strip terminal.
5b If an alarm is locked-in to scan lead under test—
   At jack, lamp, and key circuit—
   Momentarily operate MAR key.

6 At recorder transfer circuit under test—
   Connect ground or block operated relay
   associated with scan lead under test as specified
   in Table A, Test AM.

7 At recorder transfer circuit under test—
   Disconnect ground or remove blocking tool
   placed in Step 6.

8 At interface and control circuit—
   Disconnect test probe from terminal strip
   terminal.

9 Repeat Steps 4 through 8 for each scan lead
   under test as specified in Table A, Test AM.

AN. Combined/Completing Marker Circuit (SD-25550-
    01 / SD-26002-01) Marker Peg Count

4 At interface and control circuit—
   Connect test probe to terminal strip terminal
   as specified in Table A, Test AN.

5 At combined/completing marker circuit under
   test—
   Block operated relay associated with scan lead
   under test as specified in Table A, Test AN.

   Release relay operated in Step 5.

7 At interface and control circuit—
   Disconnect test probe from terminal strip
   terminal.

8 Repeat Steps 4 through 7 for each scan lead
   under Test as specified in Table A, Test AN.

AO. Dial Tone Marker Circuit (SD-25550-01/
    SD-26001-01) Marker Peg Count

4 At interface and control circuit—
   Connect test probe to terminal strip terminal
   as specified in Table A, Test AO.

5 At dial tone marker circuit under test—
   Block operated relay associated with scan lead
   under test as specified in Table A, Test AO.

   Release relay operated in Step 5.

   At interface and control circuit—
   Ground removed from terminal strip terminal.

   At interface and control circuit—
   Ground present at terminal strip terminal.

   Ground present at terminal strip terminal.

   Ground removed from terminal strip terminal.
7 At interface and control circuit—
Disconnect test probe from terminal strip terminal.

8 Repeat Steps 4 through 7 for each scan lead under test as specified in Table A, Test AO.

AP. **Electronic Dial Tone Speed Register Circuit (SD-3B504-01)**

4 At interface and control circuit—
Connect test probe to terminal strip terminal specified in Table A, Test AP.

5b If an alarm is locked-in to scan lead under test—
At master test frame jack, lamp, and key circuit—
Momentarily operate MAR key.

6 At electronic dial tone speed register circuit—
Connect ground to relay contact as specified in Table A, Test AP.

7 Disconnect ground placed in Step 6.

8 At interface and control circuit—
Disconnect test probe.

AQ. **AIOD Transverter Connector Circuit (SD-99320-01)**

3 At interface and control circuit—
Connect test probe to terminal strip terminal as specified in Table A, Test AQ.

5b If an alarm is locked-in to scan lead under test—
At master test frame jack, lamp, and key circuit—
Momentarily operate MAR key.

6 At AIOD transverter connector—
Connect ground to relay contact specified in Table A, Test AQ.

7 Disconnect ground placed in Step 6.

8 At interface and control circuit—
Disconnect test probe from terminal strip terminal.
AR. Line Concentrator Identifier (SD-95964-01)

4. At interface and control circuit—
   Connect test probe to terminal strip terminal
   as specified in Table A, Test AR.
   If an alarm locked-in to scan lead under test—
   Ground present at terminal strip terminal.

5a. If an alarm locked-in to scan lead under test—
    At master test frame jack, lamp, and key
    circuit—
    Momentarily operate MAR key.

6. At line concentrator identifier circuit under
   test—
   Block operated relay associated with scan lead
   under test as specified in Table A, Test AR.
   At interface and control circuit—
   Ground present at terminal strip terminal.


8. At interface and control circuit—
   Disconnect test probe from terminal strip
   terminal.

AS. No. 1 Trunk Concentrator (SD-97595-01)

4. At interface and control circuit—
   Connect test probe to terminal strip terminal
   as specified in Table A, Test AS.
   If an alarm locked-in to scan lead under test—
   Ground present at terminal strip terminal.

5a. If an alarm locked-in to scan lead under test—
    At master test frame jack, lamp, and key
    circuit—
    Momentarily operate MAR key.

6. At No. 1 trunk concentrator under test—
   Block operated relay associated with scan lead
   under test as specified in Table A, Test AS.
   At interface and control circuit—
   Ground present at terminal strip terminal.


8. At interface and control circuit—
   Disconnect test probe from terminal strip
   terminal.

9. Repeat Steps 4 through 8 for each scan lead
   under test as specified in Table A, Test AS.

COMMAND AND STATUS LEAD TESTS—TABLE B

BA. Dial Tone Marker Circuit (SD-25550-01/
    SD-26001-01)

4. At interface and control circuit—
   Connect test probe to terminal strip terminal
   as specified in Table B, Test BA.
   If circuit under test is made busy—
   Ground present at terminal strip terminal.
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5 At master test frame, jack, lamp and key circuit—
At circuit under test—
Insert make-busy plug in MB jack as specified in Table B, Test BA.

6 Remove MB plug placed in Step 5.

7 At interface and control circuit—
Disconnect test probe from terminal strip terminal.

8 At interface and control circuit—
Connect ground to terminal strip terminal as specified in Table B, Test BA.

9 At interface and control circuit—
Disconnect ground placed in Step 8.

10 Repeat Steps 4 through 9 for each scan lead under test as specified in Table B, Test BA.

BB. Combined/Completing Marker Circuit
(SD-25550-01/SD-26002-01)

4 At interface and control circuit—
Connect test probe as required to terminal strip terminal as specified in Table B, Test BB.

5 At master test frame, jack, lamp, and key circuit—
At circuit under test—
Make circuit under test busy by inserting MB plug in MB jack.

6 Removed MB plug placed in Step 5.

7 At interface and control circuit—
Disconnect test probe from terminal strip terminal.

8 At interface and control circuit—
Connect ground to terminal strip terminal as specified in Table B, Test BB.

9 At interface and control circuit—
Disconnect ground placed in Step 8.

10 Repeat Steps 4 through 9 for each scan lead under test as specified in Table B, Test BB.

If circuit under test is made busy—
Ground present at terminal strip terminal.

At interface and control circuit—
Ground present at terminal strip terminal.

At interface and control circuit—
Ground removed from terminal strip terminal.

At circuit under test—
MB relay operates.

At interface and control circuit—
Ground present at terminal strip terminal.

At interface and control circuit—
Ground removed from terminal strip terminal.

At circuit under test—
MB relay operates.
BC. Transverter Circuit (SD-25591-01/SD-26010-01, 
    SD-26161-01/SD-27809-01)

4 At interface and control circuit—
   Connect test probe as required, to terminal 
   strip terminal as specified in Table B, Test 
   BC.

5 At master test frame, jack, lamp, and key 
   circuit—
   At circuit under test—
   Make circuit under test busy by inserting MB 
   plug in MB jack.

6 Remove MB plug placed in Step 5.

7 At interface and control circuit—
   Disconnect test probe from terminal strip 
   terminal.

8 At interface and control circuit—
   Connect ground to terminal strip terminal as 
   specified in Table B, Test BC.

9 At interface and control circuit—
   Disconnect ground placed in Step 8.

10 Repeat Steps 4 through 9 for each scan lead 
   under test as specified in Table B, Test BC.

BD. Pretranslator (SD-25568-01)

4 At interface and control circuit—
   Connect test probe as required, to terminal 
   strip terminal as specified in Table B, Test 
   BD.

5 At master test frame jack, lamp, and key 
   circuit—
   At circuit under test—
   Make circuit under test busy by inserting MB 
   plug in MB jack.

6 Remove MB plug placed in Step 5.

7 At interface and control circuit—
   Disconnect test probe from terminal strip 
   terminal.

8 At interface and control circuit—
   Connect ground to terminal strip terminal as 
   specified in Table B, Test BD.

   If circuit under test is made busy—
   Ground present at terminal strip terminal.

   At interface and control circuit—
   Ground present at terminal strip terminal.

   At circuit under test—
   RB relay operates.

   At interface and control circuit—
   Ground removed from terminal strip terminal.

   At circuit under test—
   MB relay operates.
9. At interface and control circuit—
   Disconnect ground placed in Step 8.

10. Repeat Steps 4 through 9 for each scan lead under test as specified in Table B, Test BD.

**BE. Trouble Recorder Control Circuit (SD-25572-01)**

4. At interface and control circuit—
   Connect test probe as required, to terminal strip terminal as specified in Table B, Test BE.

5. At master test frame, jack, lamp, and key circuit—
   Make circuit under test busy by inserting MB plug in TR MB jack.

6. Remove MB plug placed in Step 5.

7. At interface and control circuit—
   Disconnect test probe from terminal strip terminal.

8. At interface and control circuit—
   Connect ground to terminal strip terminal as specified in Table B, Test BE.

9. At interface and control circuit—
   Disconnect ground placed in Step 8.

10. Repeat Steps 4 through 9 for each scan lead under test as specified in Table B, Test BE.

**BF. Line Insulation Test Circuit (SD-25796-01)**

4. At interface and control circuit—
   Connect test probe as required, to terminal strip terminal as specified in Table B, Test BF.

5. At line insulation test circuit—
   Operate TS Key.

6. At line insulation test circuit—
   Restore TS key operated in Step 5.

7. At interface and control circuit—
   Disconnect test probe from terminal strip terminal.

8. At interface and control circuit—
   Connect ground to terminal strip terminal as specified in Table B, Test BF.

If circuit under test is made busy—
   ROS relay operates.
9 At interface and control circuit—
   Disconnect ground place in Step 8.

10 Repeat Steps 4 through 9 for each scan lead under test as specified in Table B, Test BF.

**BG. Traffic Usage Recorder Circuit (SD-95738-01)**

4 At interface and control circuit—
   Connect ground to terminal strip terminal as specified in Table B, Test BG

5 At interface and control circuit—
   Disconnect ground place in Step 4.

6 Repeat Steps 4 and 5 for each scan lead under test as specified in Table B, Test BG.

**BH. Interface and Control Circuit (SD-28075-01)**

4 At interface and control circuit—
   Connect test probe as required, to terminal strip terminal as specified in Table B, Test BH.

5 At master test frame jack, lamp, and key circuit—
   Operate REM key.

6 Restore REM key operated in Step 5.

7 At interface and control circuit—
   Disconnect test probe from terminal strip terminal.

8 At interface and control circuit—
   Connect ground to terminal strip terminal as specified in Table B, Test BH.

9 At interface and control circuit—
   Disconnect ground place in Step 8.

10 Repeat Steps 4 through 9 for each scan lead under test as specified in Table B, Test BH.

**Bl. 3A Auxiliary Processor (SD-28118-01)**

*CAUTION: Refer to Section 218-799-106 for precautions to be exercised when testing ETS offices served by a Remote Alarm, Surveillance, and Control System.*
4 At interface and control circuit—
Connect ground to terminal strip terminal as specified in Table B, Test BJ.

5 At interface and control circuit—
Disconnect ground placed in Step 4.

6 Repeat Steps 4 and 5 for each scan lead under test as specified in Table B, Test BJ.

BJ. Master Test Frame, Jack, Lamp, and Key Circuit (SD-25762-01) Control Functions

4 At interface and control circuit—
Connect test probe as required, to terminal strip terminal as specified in Table B, Test BJ.

5 At circuit under test—
Operate key specified in Table B, Test BJ.

6 At circuit under test—
Release key operated in Step 5.

7 At interface and control circuit—
Disconnect test probe from terminal strip terminal.

8 At interface and control circuit—
Connect ground to terminal strip terminal as specified in Table B, Test BJ.

9 At interface and control circuit—
Disconnect ground placed in Step 8.

10 Repeat Steps 4 through 9 for each scan lead under test as specified in Table B, Test BJ.

BK. AMA Recorder Transfer Circuit (SD-28076-01)

4 At interface and control circuit—
Connect test probe as required, to terminal strip terminal as specified in Table B, Test BK.

5 At circuit under test—
Block operated relay specified in Table B, Test BK.

6 At circuit under test—
Remove blocking tool placed in Step 5.
7 At interface and control circuit—
Disconnect test probe from terminal strip terminal.

8 At interface and control circuit—
Connect ground to terminal strip terminal as specified in Table B, Test BK.

9 At interface and control circuit—
Disconnect ground placed in Step 8.

BL Interface and Control Circuit—(SD-28075-01)
Stuck Sender Hold Feature

4 At interface and control circuit—
Connect ground to terminal strip terminal as specified in Table B, Test BL.

5 At interface and control circuit—
Disconnect ground placed in Step 4.

BM Line Concentrator Identifier Circuit—(SD-95964-01)

4 At interface and control circuit—
Connect test probe to terminal strip terminal as specified in Table B, Test BM.

5 At line concentrator identifier under test—
Block operated LCIAR relay.

6 Remove blocking tool placed in Step 5.

7 At interface and control circuit—
Disconnect test probe.
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**TABLE A (Contd)**

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