REVERTIVE PULSE INCOMING REGISTER SD-25565-01
TANDEM OPERATION
TESTS USING TEST SET SD-25676-01 (J24756B)
NO. 5 CROSSBAR OFFICES

1. GENERAL

1.01 This section presents a method for testing tandem revertive pulse incoming registers using the test circuit for register and CAMA sender circuits SD-25988-01 and the test set circuit for register and CAMA sender circuits (test set) SD-25676-01.

1.02 This section is reissued for the following reasons:

(a) To add SD reference to title

(b) To revise Test A to provide for new TCA lead in marker groups arranged for interchangeable code calling

(c) To make minor changes as required.

This reissue does not affect Equipment Test Lists.

1.03 The tests covered are:

A. Registration—All Selections: The following features are checked: (1) Registration of trunk link frame number. (2) Registration of trunk number. (3) Registration of pulses on each selection. (4) Ability to recognize the completion of a selection. (5) Translation of office code and number on a 2-out-of-5 basis. (6) Ability of selecting fingers to settle down before the next selection is registered when maximum number of fingers are engaged in one direction. (7) Test calls 1 through 6 check the ability of the register to apply battery and ground to the cable conductors before returning supervision to the trunk. (8) Ability to receive calls when false grounds or trouble crosses cause the simultaneous operation of an odd- and even-numbered register trunk link frame units crosspoint.

B. Registration—Straightforward and Service Codes: The following features are checked: (1) Ability to recognize 2- and 3-digit office codes. (2) Trunk closure when preceded by trunk test and open interval. (3) Trunk closure when preceded by trunk test and no open interval.

C. Minimum Interdigital Time Test:

D. Telltale During Office Group Selection: This test checks the ability of the register to release in 1 to 2 seconds without calling in a marker.

E. Telltale During Incoming Brush Selection: This test checks the ability of the register to release immediately without calling in a marker.

F. Unused Selections: This test checks the ability of the register to send a reversed battery signal to the originating end on the first or second closure of the fundamental after an unused selection is received for any code or number.

G. Overall Timeout: This test checks that the overall timer will cause a trouble release within the required interval.
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H. Link Release: This test checks the ability of the register to cause a trouble release when the incoming register link crosspoints are not established within 1 second.

I. Common Alarm Timing:

J. Trouble Release (IRBT Key Operated):

K. Double-Connection: This test checks that the register recognizes a double-connection at the incoming register link switch.

1.04 Tests A through K are performed with the test set located at the master test frame (MTF). For trouble locating purposes, the test set may be located at the register frame and the operated or released ON relay in the register under test should be substituted for the verification of the lighted or extinguished IRON lamps, respectively.

1.05 When tests are made at the register frame or to avoid duplicate trouble records, release the ITRR key at the MTF.

1.06 If the office is equipped with both automatic monitor and incoming register test circuits, the STT/STM key of the automatic monitor should be restored before starting any tests using the incoming register test circuit. If the STT/STM key of the automatic monitor is operated while a test by the incoming register test circuit is in progress, the circuit will release as if the RL key has been operated.

1.07 $Test G requires that the RB2 relay in a register group-busy circuit be blocked operated. This will produce register group-busy timing conditions. The test should be performed during periods of light traffic.

1.08 If all other registers in the group become busy during this test, the traffic register associated with the GB lead will score. Refer to local instructions for recording these register operations.

1.09 Lettered Steps: A letter a, b, c, etc., added to a step number in Part 4 of this section indicates an action which may or may not be required, depending on local conditions. The condition under which a lettered step or a series of lettered steps should be made is given in the ACTION column, and all steps governed by the same condition are designated by the same letter within a test. Where a condition does not apply, all steps designated by that letter should be omitted.

1.10 The manner of selecting some circuits and test conditions at the MTF and its associated circuits varies depending on the apparatus options furnished with these circuits. Therefore, where variable means of selection are provided, precise instructions for the selection of circuits and test conditions are not given. Precise instructions for the use of these variable means are given in Section 218-106-301.

1.11 The location statement, At MTF—, is used to refer to all apparatus located on the four basic bays of the MTF.

2. APPARATUS

Tests A Through J

2.01 Master test control circuit SD-25800-01.

2.02 Test circuit for register and CAMA sender circuits SD-25988-01.

2.03 Test set circuit for register and CAMA sender circuits (test set) SD-25676-01.

2.04 Testing cord assembly, 20-conductor cord, 6 feet long (W20C cord) (for connecting test set circuit for register and CAMA sender circuits IRT jack to test circuit for register and CAMA sender circuits IRT jack).

2.05 322A (make-busy) plug.

Tests A, K

2.06 Testing cord, 6 feet long, equipped with two 360A tools (1W13B) and two KS-627S connecting clips (for terminal interconnections and connecting ground to terminals).

$Tests B, G, H$

2.07 KS-3008 stopwatch or equivalent.
2.08 Blocking and insulating tools as required. Use tools and apply as covered in Section 069-020-801.

3. PREPARATION

3.01 Determine from local office records the OB (office brush) and OG (office group) selections which correspond to the office codes that are cross-connected in the register.

3.01 Select full selectors office codes which include digits 0 through 4 for OB selections and digits 0 through 9 for OG selections. Record codes under corresponding heading in Table A so that OB0 and OG0 will be used in test call 1; OB1 and OG1 will be used in test call 2, etc.

3.02 If the register is equipped for ten codes or less, repeat the use of the available codes to complete the OB and OG columns of Table A.

3.03 If the register is equipped for more than ten codes, select codes so that digits 0 through 9 are used in each of the A, B, and C columns of Table A.

3.04 Select straightforward and service codes which include digits 0 through 4 for OB selections and digits 0 through 9 for OG selections along with associated A, B, and C digits. Record in Table B.

3.05 Select an office code from Table A. Record the code for OB and OG selections in Table C for test calls 1 through 3. Use the same OB selection for test calls 6 and 7 and OG selection for test calls 4 and 5.

3.06 If the register is equipped for ten or more codes, record the unequipped OB selection for test call 5 and the unequipped OG selection for test call 6.

3.07 If the register is equipped for ten codes or less, record the unequipped OG selection for test call 7.

3. PREPARATION (Cont)

<table>
<thead>
<tr>
<th>STEP</th>
<th>ACTION</th>
</tr>
</thead>
<tbody>
<tr>
<td>Tests A Through J</td>
<td></td>
</tr>
<tr>
<td>1</td>
<td>At MTF— Insert make-busy plug into IRMB jack associated with register under test.</td>
</tr>
<tr>
<td>2</td>
<td>Select incoming register.</td>
</tr>
<tr>
<td>3</td>
<td>Select trunk class.</td>
</tr>
<tr>
<td>4</td>
<td>At test set— Connect IRT jack to IRT jack of test circuit for register and CAMA sender circuits.</td>
</tr>
<tr>
<td>5</td>
<td>Operate RP key.</td>
</tr>
<tr>
<td>6</td>
<td>Set L switch to OFF.</td>
</tr>
</tbody>
</table>
4. METHOD

STEP ACTION VERIFICATION

A. Registration—All Selections

7 At MTF—
Operate ITRR key.

8 At test set—
Momentarily operate STT key.
RR lamp lighted.
At MTF—
IRT, IRON lamps lighted.

9 At test set—
Key selections for test call 1 as indicated in
Table A.

10 Operate SF-IA key.
IA-OF lamp momentarily lighted.
RR lamp extinguished.

### TABLE A

<table>
<thead>
<tr>
<th>TEST CALL</th>
<th>SELECTIONS TO BE KEYED</th>
<th>TROUBLE RECORD PERFORATIONS</th>
</tr>
</thead>
<tbody>
<tr>
<td>Call</td>
<td>OB</td>
<td>OG</td>
</tr>
<tr>
<td>1</td>
<td>0</td>
<td>0</td>
</tr>
<tr>
<td>2</td>
<td>1</td>
<td>1</td>
</tr>
<tr>
<td>3</td>
<td>2</td>
<td>2</td>
</tr>
<tr>
<td>4</td>
<td>3</td>
<td>3</td>
</tr>
<tr>
<td>5</td>
<td>4</td>
<td>0</td>
</tr>
<tr>
<td>6</td>
<td>1</td>
<td>2</td>
</tr>
<tr>
<td>7</td>
<td>0</td>
<td>3</td>
</tr>
<tr>
<td>8</td>
<td>2</td>
<td>1</td>
</tr>
<tr>
<td>9</td>
<td>3</td>
<td>0</td>
</tr>
<tr>
<td>10</td>
<td>4</td>
<td>3</td>
</tr>
<tr>
<td>11</td>
<td>0</td>
<td>2</td>
</tr>
<tr>
<td>12</td>
<td>1</td>
<td>3</td>
</tr>
<tr>
<td>13</td>
<td>2</td>
<td>0</td>
</tr>
<tr>
<td>14</td>
<td>3</td>
<td>1</td>
</tr>
<tr>
<td>15</td>
<td>4</td>
<td>2</td>
</tr>
</tbody>
</table>
11  At test set—
    Restore SF-IA key.

12  Momentarily operate RL key.

13  Repeat Steps 8 through 12 for test calls 2 through 6 indicated in Table A, using each trunk class provided.

14  Operate MAX key.

15  At test set—
    Momentarily operate STT key.

16  Key selections for test call 7 as indicated in Table A.

17  At test set—
    Operate SF-IA key.

18  At test set—
    Restore SF-IA key.

19  Momentarily operate RL key.

**VERIFICATION**

At MTF—
IRON lamp extinguished.
Trouble record taken.
Digits associated with test call 1 perforated.
Designations perforated corresponding to trunk frame number, trunk number, and trunk class.

*If office is arranged for interchangeable codes—
TCA designation perforated.
If marker group is arranged for 2-wire and 4-wire—
2W designation perforated.*

All lamps extinguished.

RR lamp lighted.
At MTF—
IRT, IRON lamps lighted.

IA-OF lamp lighted.
RR lamp extinguished.
At MTF—
IRON lamp extinguished.
Trouble record taken.
Digits associated with test call 7 perforated.
Designations perforated corresponding to trunk frame number, trunk number, and trunk class.

*If office is arranged for interchangeable codes—
TCA designation perforated.
If marker group is arranged for 2-wire and 4-wire—
2W designation perforated.*

IA-OF lamp extinguished.

All lamps extinguished.
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<table>
<thead>
<tr>
<th>STEP</th>
<th>ACTION</th>
<th>VERIFICATION</th>
</tr>
</thead>
<tbody>
<tr>
<td>20</td>
<td>Repeat Steps 15 through 19 for test calls 8 through 15 as indicated in Table A.</td>
<td></td>
</tr>
</tbody>
</table>
| 21   | At MTF—
Restore MAX, ITRR keys. |  |
| 22a  | If STR relay is provided—
At incoming register link frame—
*For nonwire-spring-relay type circuit*
Connect terminal 21 to 24 on F1 terminal strip.
*For wire-spring-relay type circuit*
Connect terminal 0 to 3 on B terminal strip for horizontal group 0. | Trouble record taken.
FT1, FT2, FT4, FT7 designations perforated. |
| 23a  | Repeat Steps 8 through 11. | All lamps extinguished. |
| 24a  | At test set—
Momentarily operate RL key. |  |
| 25   | At MTF—
Disconnect testing cord if not required in next test. |  |
| 26   | Restore all keys and switches not required in next test. |  |
| 27   | Disconnect test set from MTF if not required in next test. |  |

### B. Registration—Straightforward and Services Codes

<table>
<thead>
<tr>
<th>STEP</th>
<th>ACTION</th>
<th>VERIFICATION</th>
</tr>
</thead>
</table>
| 7    | At MTF—
Operate ITRR key. |  |
| 8    | At test set—
Momentarily operate STT key. | RR lamp lighted.
At MTF—
IRT, IRON lamps lighted. |
| 9    | At test set—
Key selections for test call 1 as indicated in Table B. |  |
| 10   | Momentarily operate (less than 1 second) SF-IA key. | RR lamp extinguished.
At MTF—
IRON lamp extinguished.
Trouble record taken.
Designations perforated corresponding to test call office. |
### TABLE B

<table>
<thead>
<tr>
<th>TEST CALL</th>
<th>SELECTIONS TO BE KEYED</th>
<th>TROUBLE RECORD PERFORATIONS</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>OB</td>
<td>OG</td>
</tr>
<tr>
<td>1</td>
<td></td>
<td></td>
</tr>
<tr>
<td>2</td>
<td></td>
<td></td>
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<tr>
<td>3</td>
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<td>4</td>
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<td>8</td>
<td></td>
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<tr>
<td>9</td>
<td></td>
<td></td>
</tr>
<tr>
<td>10</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

11. At test set—
   Momentarily operate RL key.
   All lamps extinguished.

12. Repeat Steps 8 and 9.

13. Operate SF-IA key; **start timing.**
   In 3 to 5 seconds—
   RR lamp extinguished.
   At MTF—
   IRON lamp extinguished.
   Trouble record taken.
   Designations perforated corresponding to test call office code.

14. At test set—
    Restore SF-IA key.

15. Momentarily operate RL key.
    All lamps extinguished.

16. Repeat Steps 8 through 11 for test calls 2 through 10 as indicated in Table B.

17. At MTF—
    Restore all keys and switches not required in next test.

18. Disconnect test set from MTF if not required in next test.
C. Minimum Interdigital Time Test

7 At MTF—
   Operate ITRR key.

8 At test set—
   Momentarily operate STT key.
   RR lamp lighted.
   At MTF—
   IRT, IRON lamps lighted.

9 At test set—
   Key OB and OG selections for a test call
   indicated in Table A.

10 At MTF—
    Operate FOT key.

11 At test set—
   Operate and hold key 3 for IB selection for
   test call selected in Step 9.
   P lamp flashes once, then lights steadily.

12 Release key 3.

13 Operate and hold key 2 for FB selection for
   test call selected in Step 9.
   P lamp extinguished.

14 Release key 2.

15 Key digit 3 for FU selection for test call
   selected in Step 9.
   P lamp extinguished.
   RR lamp extinguished.
   At MTF—
   IRT, IRON lamp extinguished.
   Trouble record taken.
   Designations perforated corresponding to called
   number 7723.

16 Restore all keys and switches not required in
   next test.

17 Disconnect test set from MTF if not required
   in next test.

D. Telltale During Office Group Selection

7 Momentarily operate STT key.
   RR lamp lighted.
   At MTF—
   IRT, IRON lamps lighted.

8 At test set—
   Key OB selection for a test call indicated in
   Table A.
E. Telltale During Incoming Brush Selection

7  Momentarily operate STT key.

8  At test set—
Key OB and OG selections for a test call indicated in Table A.

9  Operate SF-IA key.

10 At test set—
Restore SF-IA key.

11 Momentarily operate RL key.

12 Restore all keys and switches not required in next test.

13 Disconnect test set from MTF if not required in next test.

VERIFICATION

IA-OF lamp lighted.
At MTF—
IRON lamp remains lighted.

IA-OF, RR lamps extinguished.
At MTF—
IRON lamp extinguished.

All lamps extinguished.

F. Unused Selections

7  At MTF—
Operate MAX key.
STEP | ACTION | VERIFICATION
--- | --- | ---
8 | At test set—\nMomentarily operate STT key. | RR lamp lighted.\nAt MTF—\nIRT, IRON lamps lighted.
9 | At test set—\nKey selections for test call 1 indicated in Table C. | IA-OF lamp momentarily lighted.\nRR lamp extinguished.\nAt MTF—\nIRON lamp extinguished.
10 | At test set—\nMomentarily operate RL key. | All lamps extinguished.

11 | Repeat Steps 8 through 10 for all selections in test call 1 through 3 indicated in Table C. |

12a | If register is equipped for ten or more codes—\nRepeat Steps 8 through 10 for all selections in test call 4 through 6 indicated in Table C. |

13b | If register is equipped for ten or less codes—\nRepeat Steps 8 through 10 for test call 7. |

### TABLE C

<table>
<thead>
<tr>
<th>TEST CALL</th>
<th>SELECTIONS TO BE KEYED</th>
<th>IA-OF LAMP MOMENTARILY LIGHTED DURING</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>OB</td>
<td>OG</td>
</tr>
<tr>
<td>1 Overstep IB</td>
<td></td>
<td></td>
</tr>
<tr>
<td>2 Overstep IG</td>
<td></td>
<td></td>
</tr>
<tr>
<td>3 Overstep FB</td>
<td>2</td>
<td></td>
</tr>
<tr>
<td>4 Overstep OB</td>
<td></td>
<td></td>
</tr>
<tr>
<td>5 Unequipped OB</td>
<td></td>
<td></td>
</tr>
<tr>
<td>6 Unequipped OG</td>
<td></td>
<td></td>
</tr>
<tr>
<td>7 Unequipped OG</td>
<td></td>
<td></td>
</tr>
<tr>
<td>STEP</td>
<td>ACTION</td>
<td>VERIFICATION</td>
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<tr>
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<td>--------------</td>
</tr>
<tr>
<td>14</td>
<td>At MTF—&lt;br&gt;Restore all keys and switches not required in next test.</td>
<td>RR lamp lighted.&lt;br&gt;At MTF—&lt;br&gt;IRT, IRON lamps lighted.</td>
</tr>
<tr>
<td>15</td>
<td>Disconnect test set from MTF if not required in next test.</td>
<td>RR lamp lighted.&lt;br&gt;At MTF—&lt;br&gt;IRT, IRON lamps lighted.</td>
</tr>
</tbody>
</table>

**G. Overall Timeout**

7. At register circuit associated with test—<br>Block nonoperated OVL relay.<br>R Kerr lamp lighted.<br>At MTF—<br>IRT, IRON lamps lighted.<br>P Kerr lamp momentarily lighted.<br>At 20 to 32 seconds—<br>IRT lamp extinguished.<br>All lamps extinguished.<br>

8. At test set—<br>Momentarily operate STT key.<br>P Kerr lamp lighted.<br>At MTF—<br>IRT, IRON lamps lighted.<br>P Kerr lamp momentarily lighted.<br>At 20 to 32 seconds—<br>IRT lamp extinguished.<br>All lamps extinguished.<br>

9. At test set—<br>Key OB selection for a test call indicated in Table A; **start timing**.<br>P Kerr lamp lighted.<br>At MTF—<br>IRT, IRON lamps lighted.<br>P Kerr lamp momentarily lighted.<br>At 20 to 32 seconds—<br>IRT lamp extinguished.<br>All lamps extinguished.<br>

10. After 12 seconds—<br>Key OG selection for the selected call; **start timing**.<br>P Kerr lamp lighted.<br>At MTF—<br>IRT, IRON lamps lighted.<br>P Kerr lamp momentarily lighted.<br>At 20 to 32 seconds—<br>IRT lamp extinguished.<br>All lamps extinguished.<br>

11. After 12 seconds—<br>Key IB selection for the selected call; **start timing**.<br>P Kerr lamp lighted.<br>At MTF—<br>IRT, IRON lamps lighted.<br>P Kerr lamp momentarily lighted.<br>At 20 to 32 seconds—<br>IRT lamp extinguished.<br>All lamps extinguished.<br>

12. At test set—<br>Momentarily operate RL key.<br>P Kerr lamp lighted.<br>At MTF—<br>IRT, IRON lamps lighted.<br>P Kerr lamp momentarily lighted.<br>At 20 to 32 seconds—<br>IRT lamp extinguished.<br>All lamps extinguished.<br>

13. At register circuit under test—<br>Remove blocking tool from OVL relay.<br>P Kerr lamp lighted.<br>At MTF—<br>IRT, IRON lamps lighted.<br>P Kerr lamp momentarily lighted.<br>At 20 to 32 seconds—<br>IRT lamp extinguished.<br>All lamps extinguished.<br>

14. At test set—<br>Momentarily operate STT key; **start timing**.<br>P Kerr lamp lighted.<br>At MTF—<br>IRT, IRON lamps lighted.<br>P Kerr lamp momentarily lighted.<br>At 20 to 32 seconds—<br>IRT lamp extinguished.<br>All lamps extinguished.<br>

15. At test set—<br>Momentarily operate RL key.<br>P Kerr lamp lighted.<br>At MTF—<br>IRT, IRON lamps lighted.<br>P Kerr lamp momentarily lighted.<br>At 20 to 32 seconds—<br>IRT lamp extinguished.<br>All lamps extinguished.<br>

16. At group-busy circuit associated with register under test—<br>Block operated RB2 relay.<br>P Kerr lamp lighted.<br>At MTF—<br>IRT, IRON lamps lighted.<br>P Kerr lamp momentarily lighted.<br>At 20 to 32 seconds—<br>IRT lamp extinguished.<br>All lamps extinguished.<br>

17. At test set—<br>Momentarily operate STT key.<br>P Kerr lamp lighted.<br>At MTF—<br>IRT, IRON lamps lighted.
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STEP                ACTION                                                                                     VERIFICATION

 18                At test set—  
                   Key OB selection for a test call indicated in  
                   Table A; *start timing*.                                P lamp momentarily lighted.

 19                At test set—  
                   Momentarily operate RL key.                           At MTF—

 20                At group-busy circuit associated with register under test—  
                   Remove blocking tool from RB2 relay.                 In 4 to 7 seconds—
                                                                 Iron lamp extinguished.

 21                At MTF—  
                   Restore all keys and switches not required in  
                   next test.                                          All lamps extinguished.

 22                Disconnect test set from MTF if not required in next test.

H. Link Release

 7                At register and CAMA sender circuit associated with test—  
                   Set TCL switch to OFF.                                 At MTF—
                                                                 IRT, IROn lamps lighted.
                                                                 In 1 second—
                                                                 IROn lamp extinguished.
                                                                 Trouble record taken.
                                                                 LR-, FR-, CN-, RG-, ∙2W, TCA ∙ designations perforated.

 8                At MTF—  
                   Operate ITRR key.                                     At MTF—
                                                                 IROn lamps lighted.
                                                                 In 1 second—
                                                                 IROn lamp extinguished.
                                                                 Trouble record taken.
                                                                 LR-, FR-, CN-, RG-, ∙2W, TCA ∙ designations perforated.

 9                At test set—  
                   Momentarily operate STT key.                            At MTF—
                                                                 IRT, IROn lamps lighted.
                                                                 In 1 second—
                                                                 IROn lamp extinguished.
                                                                 Trouble record taken.
                                                                 LR-, FR-, CN-, RG-, ∙2W, TCA ∙ designations perforated.

 10               Restore all keys and switches not required in next test.

 11               Disconnect test set from MTF if not required in next test.

I. Common Alarm Timing

 7                At register circuit associated with test—  
                   Block nonoperated OVL, TRL relays.                     RR lamp lighted.

 8                Insulate 3B of M relay.                          At MTF—

 9                At test set—  
                   Momentarily operate STT key.                            IRT, IROn lamps lighted.

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<table>
<thead>
<tr>
<th>STEP</th>
<th>ACTION</th>
<th>VERIFICATION</th>
</tr>
</thead>
<tbody>
<tr>
<td>10</td>
<td>At test set—&lt;br&gt;Key selections for a test call indicated in Table A; <strong>start timing.</strong></td>
<td>P lamp lighted.&lt;br&gt;At MTF—&lt;br&gt;In 20 to 32 seconds—&lt;br&gt;TO lamp lighted.</td>
</tr>
<tr>
<td>11</td>
<td>Remove make-busy plug from IRMB jack; <strong>start timing.</strong></td>
<td>In 10 to 15 seconds—&lt;br&gt;R-S-TOA lamp lighted.</td>
</tr>
<tr>
<td></td>
<td>At register circuit associated with test—&lt;br&gt;Remove blocking tools from OVL, TRL relays.</td>
<td><strong>Note:</strong> If R-S-TOA lamp is lighted in less than 10 seconds, repeat the test and assure that another circuit has not seized the common alarm circuit.</td>
</tr>
<tr>
<td>12</td>
<td>Remove insulator from M relay.</td>
<td>All lamps extinguished.</td>
</tr>
<tr>
<td>13</td>
<td>At test set—&lt;br&gt;Momentarily operate RL key.</td>
<td></td>
</tr>
<tr>
<td>14</td>
<td>At MTF—&lt;br&gt;Restore all keys and switches not required in next test.</td>
<td></td>
</tr>
<tr>
<td>15</td>
<td>Disconnect test set from MTF if not required in next test.</td>
<td></td>
</tr>
<tr>
<td></td>
<td><strong>J. Trouble Release—IRBT Key Operated</strong></td>
<td></td>
</tr>
<tr>
<td>7</td>
<td>At MTF—&lt;br&gt;Operate IRBT key.</td>
<td>RR lamp lighted.&lt;br&gt;At MTF—&lt;br&gt;IRT, IRON lamps lighted.</td>
</tr>
<tr>
<td>8</td>
<td>At test set—&lt;br&gt;Momentarily operate STT key.</td>
<td></td>
</tr>
<tr>
<td>9</td>
<td>At test set—&lt;br&gt;Key selections for a test call indicated in Table A.</td>
<td></td>
</tr>
<tr>
<td>10</td>
<td>Operate SF-IA key.</td>
<td>IA-OF lamp momentarily lighted.&lt;br&gt;RR lamp lighted.&lt;br&gt;At MTF—&lt;br&gt;IRT, IRON lamps lighted.</td>
</tr>
<tr>
<td>11</td>
<td>At test set—&lt;br&gt;Restore SF-IA key.</td>
<td></td>
</tr>
<tr>
<td>12</td>
<td>Momentarily operate RL key.</td>
<td>All lamps extinguished.</td>
</tr>
</tbody>
</table>
SECTION 218-139-505

STEP ACTION VERIFICATION

13 At MTF— Restore all keys and switches not required in next test.

14 Disconnect test set from MTF if not required in next test.

K. Double-Connection

1 At MTF— Insert make-busy plug into IRMB jack associated with register used for test.

2 Operate IRBT key.

3 At register control unit associated with test— Connect ground to HM lead (terminal 51 for register 0 or terminal 221 for register 1).

4 Block nonoperated TRL relay.

5 Manually operate ON relay.

6 Release ON relay.

7 Remove blocking tool from TRL relay.

8 Disconnect ground from HM lead.

9 Block operated H relay.

10 Manually operate ON relay.

11 Release ON relay.

12 At MTF— Restore IRBT key.

14 Remove make-busy plug from IRMB jack.

H, RV3 relays operated.

In 1 second—
TC1, TC2 relays operated.
DCK relay not operated.

RV3 relay operated.

DCK relay operated.