DISTRICT JUNCTORS
INTERRUPTER CHECKING CIRCUIT ALARMS
TESTS
NO. 1 CROSSBAR OFFICES

1. GENERAL
1.01 This section covers a method of testing the alarm features of the interrupter checking circuit associated with district junctors in No. 1 crossbar dial offices.
1.02 This section is reissued to bring it in conformity with other material in the Plant Series. In this process marginal arrows have been omitted.
1.03 The tests covered are:
   (A) Alarm due to failure of interrupter relays to operate.
   (B) Alarm due to failure of interrupter relays to release.
1.04 The tests covered by this section should not be made during periods of heavy traffic because tests herein may delay the charging of calls handled by working circuits.

2. APPARATUS
2.01 No. 508A Tool (relay blocking tool).
2.02 Stop watch or seconds indicating watch.
2.03 No. 528 Test Receiver and a W2AB (2W21A) cord equipped with two No. 365 Tools (connecting clips).

3. METHOD
   (A) Alarm Due to Failure of Interrupter Relays to Operate

3.01 Block the EB relay associated with the lowest subgroup of 10 subscriber district junctors non-operated using a No. 508A tool.
3.02 Observe that after an interval of approximately 3 to 13 seconds the PC (pulse count) lamp on the district junctor frame under test is lighted. The white aisle and main aisle pilot lamps should light and the major audible alarm should sound.
3.03 Remove the No. 508A tool from the blocked EB relay.
3.04 Operate the RL (release) key for the interrupter checking circuit at the floor alarm cabinet and observe that the visual and audible alarms are retired.
3.05 Proceed in accordance with 3.01 to 3.04 inclusive until all of the EB, EF, OB, and OF relays in all of the subscriber district junctor subgroups on the frame have been tested.

   Note: The aisle and main aisle pilot lamps and audible alarm need be checked only when making tests on one subgroup of district junctors per district junctor frame.

   (B) Alarm Due to Failure of Interrupter Relays to Release

3.06 Make sure that the OB, OF, EB and EF relays are following the CH interrupter cycle by operating and releasing once approximately every 3 seconds.

   OF Relays
3.07 By means of the headset, ground the lead to the front contact of the odd frame CH interrupter either by connecting to spring 1I of one of the OB relays or by connecting to the proper punching of the vertical terminal strip. All of the OF relays should be operated and the OB relays should all be normal and should not be following interrupter pulses.
3.08 Observe that after an interval of approximately 6 to 20 seconds the PC lamp on the district junctor frame under test is lighted. The white aisle and main
aisle pilot lamps should light and the major alarm should sound.

3.09 Remove the ground from the lead to the CH interrupter and check that the interrupter relays resume their operate and release cycle. Operate the RL key for the interrupter checking circuit at the floor alarm cabinet and observe that the visual and audible alarms are retired.

**EF Relays**

3.10 By means of the headset, ground the lead to the front contact of the even frame CH interrupter either by connecting to spring LT of one of the EF relays or by connecting to the proper punching of the vertical terminal strip. All of the EF relays should be operated and the EF relays should all be normal and should not be following interrupter pulses.

3.11 Proceed as in 3.08 and 3.09.

**OB Relays**

3.12 By means of the headset, ground the lead to the back contact of the odd frame CH interrupter either by connecting to spring LT of one of the OF relays or by connecting to the proper punching of the vertical terminal strip. All of the OF relays should be operated and the OF relays should all be normal and should not be following interrupter pulses.

3.13 Proceed as in 3.08 and 3.09.

**EB Relays**

3.14 By means of the headset, ground the lead to the back contact of the even frame CH interrupter either by connecting to spring LT of one of the EF relays or by connecting to the proper punching of the vertical terminal strip. All of the EF relays should be operated and the EF relays should all be normal and should not be following interrupter pulses.

3.15 Proceed as in 3.08 and 3.09.

**4. REPORTS**

4.01 The required record of these tests should be entered on the proper form.