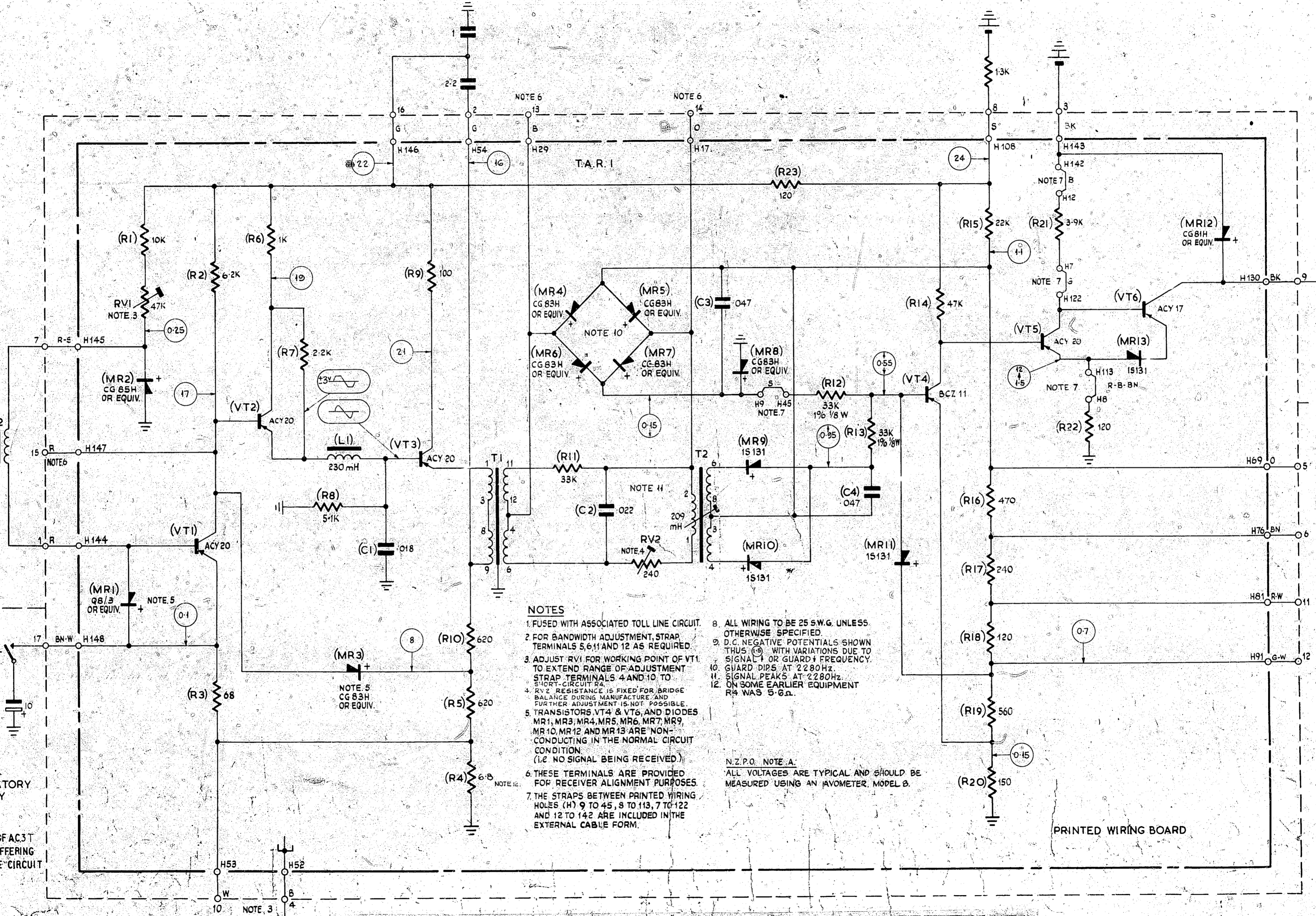


EXPLANATORY ONLY
PART OF ASSOCIATED
TOLL LINE CIRCUIT



NOTES

1. FUSED WITH ASSOCIATED TOLL LINE CIRCUIT.
2. FOR BANDWIDTH ADJUSTMENT, STRAP TERMINALS 5, 6, 11 AND 12 AS REQUIRED.
3. ADJUST RV1 FOR WORKING POINT OF VT1 TO EXTEND RANGE OF ADJUSTMENT STRAP TERMINALS 4 AND 10 TO SHORT-CIRCUIT R4.
4. RV2 RESISTANCE IS FIXED FOR BRIDGE BALANCE DURING MANUFACTURE AND FURTHER ADJUSTMENT IS NOT POSSIBLE.
5. TRANSISTORS VT4 & VT6 AND DIODES MR1, MR3, MR4, MR5, MR6, MR7, MR9, MR10, MR12 AND MR13 ARE NON-CONDUCTING IN THE NORMAL CIRCUIT CONDITION (I.E. NO SIGNAL BEING RECEIVED).
6. THESE TERMINALS ARE PROVIDED FOR RECEIVER ALIGNMENT PURPOSES.
7. THE STRAPS BETWEEN PRINTED WIRING HOLES (H) 9 TO 45, 8 TO 113, 7 TO 122 AND 12 TO 142 ARE INCLUDED IN THE EXTERNAL CABLE FORM.
8. ALL WIRING TO BE 25 S.W.G. UNLESS OTHERWISE SPECIFIED.
9. D.C. NEGATIVE POTENTIALS SHOWN THUS (⊖) WITH VARIATIONS DUE TO SIGNAL ↑ OR GUARD ↑ FREQUENCY.
10. GUARD DIPS AT 2280HZ.
11. SIGNAL PEAKS AT 2280HZ.
12. ON SOME EARLIER EQUIPMENT R4 WAS 5.6Ω.

N.Z.P.O. NOTE A.

ALL VOLTAGES ARE TYPICAL AND SHOULD BE MEASURED USING AN AVOMETER, MODEL B.

NZPO	AMENDMENT PARTS	1	2	3	4	5	6	7	8	9	10	11	12
	DATE												
DRW	CHECKED	WIRING	CIRCUIT	ASHT									
GBW	16560	SIZE M	50V										

EXPLANATORY ONLY
PART OF AC3T
TRUNK OFFERING
TOLL LINE CIRCUIT

PRINTED WIRING BOARD