

A. E. Co. TYPE 101 DIRECTOR  
OPERATION

1. GENERAL

1.01 This Addendum is issued to make changes in Section 240-205-105, Issue 2, in the General System Practices. Using ink or red pencil, make the changes as indicated in Part 2. In the margin adjacent to these changes, write "See Addendum." This addendum should then be filed ahead of Section 240-205-105.

2. CHANGES

2.01 Cross out Figure 4. The new Figure 4 changes the positions of route diode pins

V7/ZAR and V9/WAR to V7/BB-AR and V9/VAR, respectively. Designation, NOT GATE, is also changed to read NOT GATE 85.

2.02 The first two sentences in the left column at the top of page 7 (Paragraph 4.05) should read: "In position BB the last digit is outputted. In the register-sender's time slot, the route diode pin at the intersection of V7/BB-AR extends negative battery to level AR at position BB."

2.03 The last sentence of Paragraph 5.05 should read: "...and the AR mark is in position V."

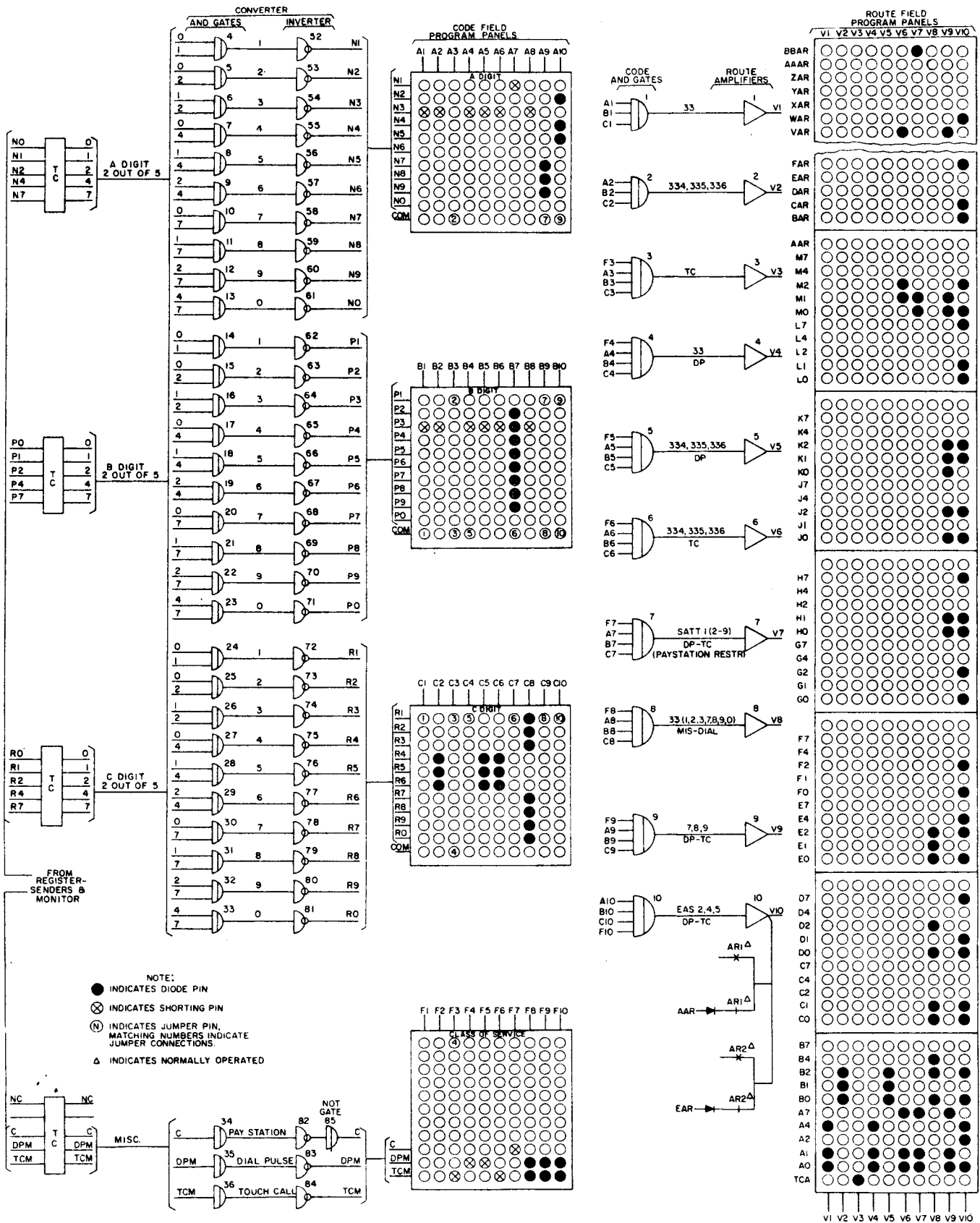


Figure 4. Translator Interconnections.